TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (U-MOS II)

TPCS8101

Lithium Ion Battery Applications Portable Equipment Applications Notebook PCs

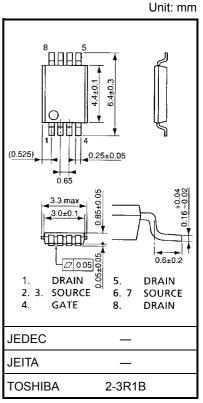
- Small footprint due to small and thin package
- Low drain-source ON resistance: $RDS(ON) = 15 \text{ m}\Omega$ (typ.)
- High forward transfer admittance: $|Y_{fs}| = 12 S$ (typ.)
- Low leakage current: $I_{DSS} = -10 \mu A \text{ (max) (V}_{DS} = -30 \text{ V)}$
- Enhancement-mode: $V_{th} = -0.8 \sim -2.0 \text{ V (V}_{DS} = -10 \text{ V, I}_{D} = -1 \text{ mA})$

Maximum Ratings (Ta = 25°C)

Characte	ristics	Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	-30	V
Drain-gate voltage (R	$k_{GS} = 20 \text{ k}\Omega$	V_{DGR}	-30	V
Gate-source voltage		V_{GSS}	±20	V
Drain current	DC (Note 1)	ΙD	-6	А
Diam curcii	Pulse (Note 1)	I_{DP}	-24	^
Drain power dissipati	on (t = 10 s) (Note 2a)	P_{D}	1.5	W
Drain power dissipati	on (t = 10 s) (Note 2b)	P_{D}	0.6	W
Single pulse avalance	ne energy (Note 3)	E _{AS}	46.8	mJ
Avalanche current		I _{AR}	-6	Α
Repetitive avalanche	energy (Note 2a, Note 4)	E _{AR}	0.15	mJ
Channel temperature		T _{ch}	150	°C
Storage temperature	range	T _{stg}	−55 to 150	°C

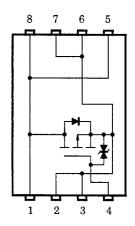
Note: For (Note 1), (Note 2), (Note 3) and (Note 4), please refer to the next page.

This transistor is an electrostatic sensitive device. Please handle with caution.



Weight: 0.035 g (typ.)

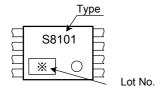
Circuit Configuration



Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient (t = 10 s) (Note 2a)	R _{th (ch-a)}	83.3	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	R _{th (ch-a)}	208	°C/W

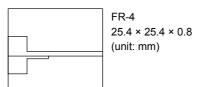
Marking (Note 5)



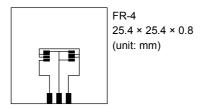
Note 1: Please use devices on condition that the channel temperature is below 150°C.

Note 2:

a) Device mounted on a glass-epoxy board (a)



b) Device mounted on a glass-epoxy board (b)

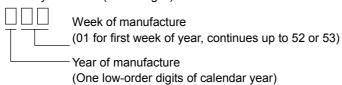


Note 3: V_{DD} = -24 V, T_{ch} = 25°C (initial), L = 1.0 mH, R_G = 25 Ω , I_{AR} = -6.0 A

Note 4: Repetitive rating: pulse width limited by maximum channel temperature

Note 5: O on lower right of the marking indicates Pin 1.

Weekly code: (Three digits)



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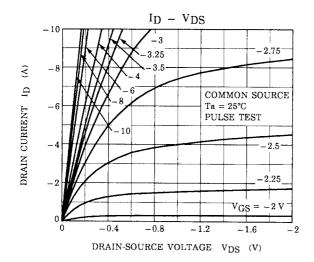
Electrical Characteristics (Ta = 25°C)

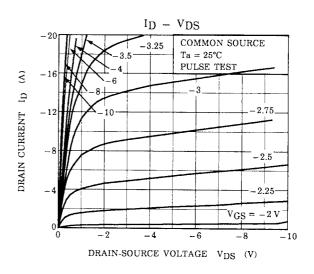
Chara	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage curi	rent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	_	_	±10	μΑ
Drain cut-OFF cu	rrent	I _{DSS}	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$	_	_	-10	μA
Drain-source breakdown voltage		V (BR) DSS	I _D = -10 mA, V _{GS} = 0 V -30		_		V
Brain-30dice bice	rain-source breakdown voltage		$I_D = -10 \text{ mA}, V_{GS} = 20 \text{ V}$	-15	—		v
Gate threshold vo	$V_{(BR)DSX}$ $I_D = -10 \text{ m}$ threshold voltage V_{th} $V_{DS} = -10 \text{ m}$		$V_{DS} = -10 \text{ V}, I_D = -1 \text{ mA}$	-0.8	—	-2.0	V
Drain source ON	rosistanco	R _{DS} (ON)	$V_{GS} = -4 \text{ V}, I_D = -3 \text{ A}$	_	32	40	mΩ
Drain-source ON resistance		R _{DS} (ON)	$V_{GS} = -10 \text{ V}, I_D = -3 \text{ A}$	_	15	25	mu
Forward transfer a	Forward transfer admittance $ Y_{fs} $ $V_{DS} = -10 \text{ V}$		$V_{DS} = -10 \text{ V}, I_D = -3 \text{ A}$	6	12	_	S
Input capacitance		C _{iss}		_	1810	_	pF
Reverse transfer capacitance		C _{rss}	V _{DS} = -10 V, V _{GS} = 0 V, f = 1 MHz	_	350	_	pF
Output capacitance		C _{oss}		_	610	_	pF
Switching time	Rise time	t _r	$V_{GS} = \begin{array}{c} 0 \text{ V} & I_{D} = -3 \text{ A} \\ V_{OUT} & V_{OUT} \\ \downarrow & \downarrow \\ V_{DD} = 5 \Omega \\ V_{DD} = -15 \text{ V} \\ \text{Duty} \leq 1\%, \ t_{W} = 10 \mu\text{s} \\ \end{array}$	_	9	1	
	Turn-ON time	t _{on}		_	15	_	ns
	Fall time	t _f		_	49		115
	Turn-OFF time	t _{off}		_	135	_	
Total gate charge (gate-source plus gate-drain)		Qg			37		nC
Gate-source charge		Q _{gs}	$V_{DD} \approx -24 \text{ V}, V_{GS} = -10 \text{ V}, I_D = -6 \text{ A}$	_	30	_	nC
Gate-drain ("miller") charge		Q _{gd}		_	7		nC

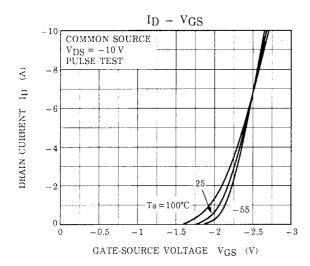
Source-Drain Ratings and Characteristics (Ta = 25°C)

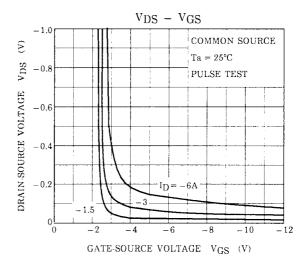
Charact	eristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Drain reverse current	Pulse (Note 1)	I _{DRP}	-	_	_	-24	Α
Forward voltage	(diode)	V_{DSF}	$I_{DR} = -6 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	1.2	V

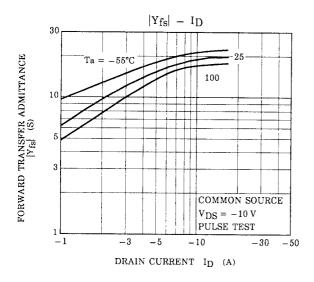
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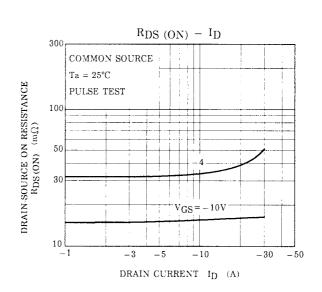




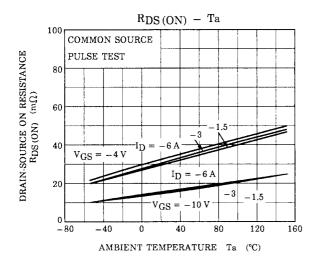


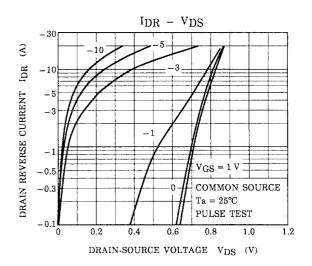


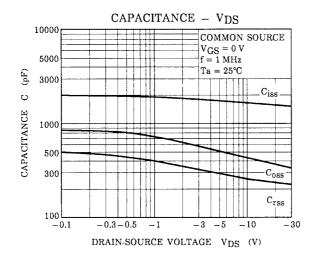


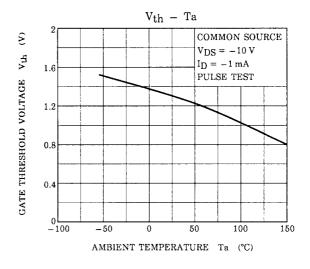


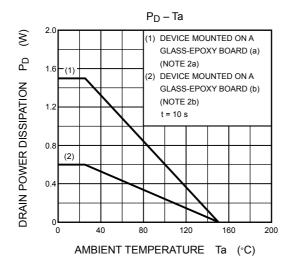
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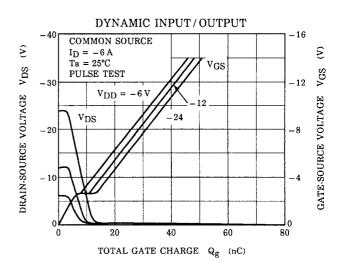




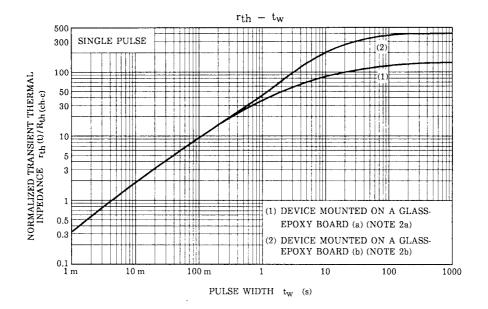


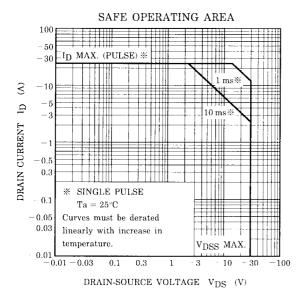


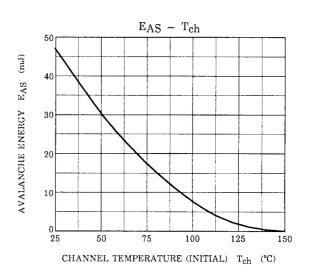


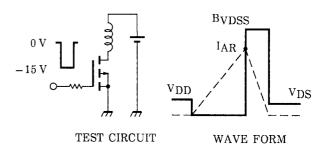


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$$\begin{array}{l} T_{ch}=25^{\circ}C~(Initial)\\ Peak~I_{AR}=-6~A,~R_{G}=25~\Omega~E_{AS}=\frac{1}{2}\cdot L\cdot I^{2}\cdot (\frac{BVDSS}{BVDSS-VDD})\\ V_{DD}=-16~V,~L=1.0~mH \end{array}$$

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