Preliminary TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

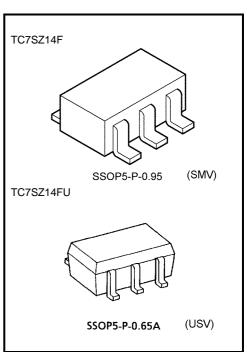
TC7SZ14F,TC7SZ14FU

Schmitt Inverter

Features

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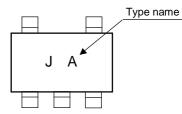
- High output drive: ±24 mA (min) @V_{CC} = 3 V
- High speed: $t_{pd} = 3.7 \text{ ns} (typ.) @V_{CC} = 5 \text{ V}, 50 \text{ pF}$
- Wide operating voltage range: $V_{CC (opr)} = 1.65$ to 5.5 V
- High latch-up immunity: Higher than or equal to ±500 mA
 - High ESD : Higher than or equal to ±200 V (JEITA) : Higher than or equal to ±2000 V (MIL)
- Power-down protection is provided on all inputs and outputs.
- Matches the performance of TC74LCX Series when operated at 3.3 V $\,$



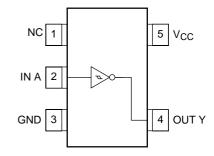
Weight:

SSOP5-P-0.95 : 0.016 g (typ.) SSOP5-P-0.65A : 0.006 g (typ.)

Marking



Pin Assignment (top view)

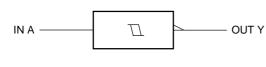


Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	-0.5 to 6	V
DC input voltage	V _{IN}	-0.5 to 6	V
DC output voltage	V _{OUT}	-0.5 to 6	V
Input diode current	IIK	-20	mA
Output diode current	I _{OK}	-20	mA
DC output current	I _{OUT}	±50	mA
DC V _{CC} /ground current	Icc	±50	mA
Power dissipation	PD	200	mW
Storage temperature	T _{stg}	-65 to 150	°C
Lead temperature (10 s)	TL	260	°C

<u>TOSHIBA</u>

Logic Diagram



А	Y
L	Н
Н	L

Truth Table

Recommended Operating Conditions

Characteristics	Symbol	Rating	Unit	
Supply voltage	Vee	1.65 to 5.5	V	
Supply voltage	Vcc	1.5 to 5.5 (Note 1)	v	
Input voltage	V _{IN}	0 to 5.5	V	
Output voltage	Vout	0 to 5.5 (Note 2)	V	
Output voltage	V001	0 to V_{CC} (Note 3)	v	
Operating temperature	T _{opr}	-40 to 85	°C	

Note 1: Date retention only

Note 2: $V_{CC} = 0 V$

Note 3: High or Low State

Electrical Characteristics

DC Electrical Characteristics

Characteristics	Symbol	Test Condition			Ta = 25°C)	Ta = -4	Unit	
Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Onin
Positive threshold voltage	VP		1.65	0.6	1.0	1.4	0.65	1.4	
			1.8	0.7	1.1	1.5	0.7	1.5	
			2.3	1.0	1.4	1.8	1.0	1.8	
			3.0	1.3	1.75	2.2	1.3	2.2	
			4.5	1.9	2.45	3.1	1.9	3.1	
			5.5	2.2	2.9	3.6	2.2	3.6	V
	V _N	_	1.65	0.2	0.5	0.8	0.2	0.8	
			1.8	0.25	0.55	0.9	0.25	0.9	
Negative threshold valtage			2.3	0.40	0.75	1.15	0.40	1.15	
Negative threshold voltage			3.0	0.6	1.0	1.5	0.6	1.5	
			4.5	1.0	1.43	2.0	1.0	2.0	
			5.5	1.2	1.70	2.4	1.2	2.4	
	VH		1.65	0.1	0.48	0.9	0.1	1.0	
Hysteresis voltage			1.8	0.15	0.54	1.0	0.15	1.0	
			2.3	0.25	0.65	1.1	0.25	1.1	V
			3.0	0.4	0.77	1.2	0.4	1.2	
			4.5	0.6	1.01	1.5	0.6	1.5	
			5.5	0.7	1.18	1.7	0.7	1.7	

Characteristics	Sumbol	Teet (Condition			Ta = 25°C)	Ta = -4	Unit	
Characteristics	Symbol	Test Condition		V _{CC} (V)	Min	Тур.	Max	Min		Max
				1.65	1.55	1.65	_	1.55	—	
				1.8	1.7	1.8		1.7	_	
			$I_{OH} = -100 \ \mu A$	2.3	2.2	2.3	_	2.2	_	
				3.0	2.9	3.0	_	2.9	—	
High-level output voltage	Vон	$V_{IN} = V_{IL}$		4.5	4.4	4.5	_	4.4	—	
	VОН	VIN – VIL	I _{OH} = -4 mA	1.65	1.29	1.52		1.29	_	
			I _{OH} = -8 mA	2.3	1.9	2.15		1.9	_	
			$I_{OH} = -16 \text{ mA}$	3.0	2.4	2.8	_	2.4	_	
			I _{OH} = -24 mA	3.0	2.3	2.68		2.3	_	
		IOH	I _{OH} = -32 mA	4.5	3.8	4.2		3.8	_	V
		VIN = VIH	I _{OL} = 100 μA	1.65		0	0.1	_	0.1	
				1.8		0	0.1	_	0.1	
				2.3		0	0.1	_	0.1	
				3.0		0	0.1	_	0.1	
Low-level output voltage	V _{OL}			4.5	_	0	0.1	_	0.1	
Low-level output voltage	VOL		I _{OL} = 4 mA	1.65	_	0.08	0.24	_	0.24	
			I _{OL} = 8 mA	2.3		0.1	0.3	_	0.3	
			I _{OL} = 16 mA	3.0		0.15	0.4	_	0.4	
			I _{OL} = 24 mA	3.0	_	0.22	0.55	_	0.55	
			I _{OL} = 32 mA	4.5	_	0.22	0.55	_	0.55	
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0~5.5			±1	_	±10	μA
Power OFF leakage current	IOFF	V_{IN} or $V_{OUT} = 5.5 V$		0.0	_	_	1	_	10	μA
Quiescent supply current	ICC	V _{IN} = 5.5 V or GND		1.65~5.5			1	_	10	μA

AC Electrical Characteristics (Unless otherwise specified Input: $t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40~85°C		Linit
			$V_{CC}(V)$	Min	Тур.	Max	Min	Max	Unit
Propagation delay time	^t pLH tpHL	\mathbf{K} = 1 IVIS2	1.65	2.0	9.1	15.0	2.0	15.6	ns
			1.8	2.0	7.6	12.5	2.0	13	
			$\textbf{2.5}\pm\textbf{0.2}$	1.0	5.0	9.0	1.0	9.5	
			$\textbf{3.3}\pm\textbf{0.3}$	1.0	3.7	6.3	1.0	6.5	
			5.0 ± 0.5	0.5	3.1	5.2	0.5	5.5	
		$C_L = 50 \text{ pF},$ $R_L = 500 \Omega$	$\textbf{3.3}\pm\textbf{0.3}$	1.5	4.4	7.2	1.5	7.5	
			5.0 ± 0.5	0.5	3.7	5.9	0.8	6.2	
Input capacitance	C _{IN}	—		_	_		_	_	pF
Power dissipation capacitance	C _{PD}		(Note 4)	_	—		_	_	pF

Note 4: CPD is defined as the value of the internal equivalent capacitance which is Calculated from the operating current consumption without load.

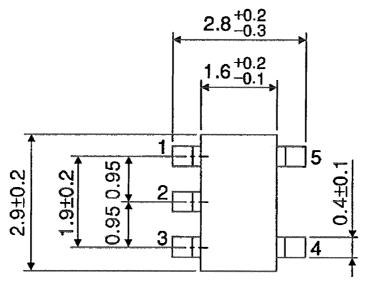
Average operating current can be obtained by the equation.

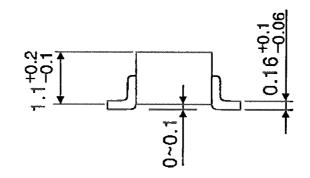
 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

Package Dimensions

SSOP5-P-0.95

Unit : mm

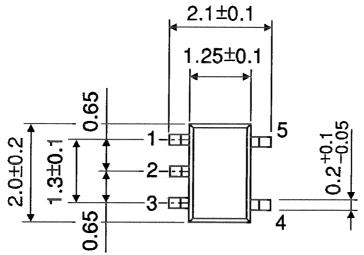


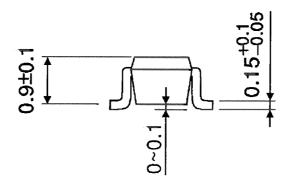


Weight: 0.016 g (typ.)

Package Dimensions

Unit : mm





Weight: 0.006 g (typ.)

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Handbook" etc..

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