# **Evaluation Board 1 SKYPER™ 32PRO**

# **Technical Explanations**

Revision 01
Status: evaluation board

This Technical Explanation is valid for the following parts:

part number	type	date code (YYWW)
L6100230	Board 1 SKYPER™ 32PRO	≥ 0522

#### Related documents:

title	version
Technical Explanations SKYPER™ 32PRO	≥ Rev02

Prepared by: Markus Hermwille

### Content

Disclaimer	2
Application and Handling Instructions	
Further application support	
General Description	
Dimensions	4
Component Placement Layout	4
PIN Array (not SKiiP® compatible)	
PIN Array – Secondary Side	
Signal IF_CMN_nHALT	
Setting Dead Time	
Setting Dynamic Short Circuit Protection	
Collector Series Resistance	
Adaptation Gate Resistors	8
Setting Soft Turn-Off	8
Over Temperature Protection Circuit (OTP)	9
Mounting Notes	
Schematics	
	12
Parts List	13

Information furnished in this document is believed to be accurate and reliable. However, no representation or warranty is given and no liability is assumed with respect to the accuracy or use of such information. Furthermore, this technical information specifies semiconductor devices but promises no characteristics. No warranty or guarantee expressed or implied is made regarding delivery, performance or suitability. Specifications mentioned in this document are subject to change without notice. This document supersedes and replaces all information previously supplied and may be supersede by updates.

#### **Disclaimer**

In accordance with SEMIKRON's quality procedures, we hereby notify you that the Evaluation Board 1 SKYPER™ 32PRO should be considered as evaluation products only.

Evaluation products are experimental products and are therefore only intended for device evaluation. SEMIKRON does not represent or guarantee that a final version will be made available after device evaluation. Evaluation products are subject to the change service.

Although evaluation products have been manufactured using processes and procedures representative of final production, they have not been subjected to all of our normal quality audits and controls. Additionally, neither the product nor the manufacturing processes may have passed our internal qualification procedures.

#### IMPORTANT:

The evaluation boards are supplied without warranty of any kind, expressed, implied of statutory, including but not limited to, any implied warranty of merchantability of fitness for a particular purpose. Credit or replacement for evaluation products that fail to function will not be given nor will a failure analysis be performed. There is no entitlement to technical support for evaluation products.

Claims for damages and reimbursement of expenses on the part of the undersigned customer shall be excluded, regardless of the legal cause, especially claims in contract and in tort.

The liability exclusions shall not apply in cases of liability independent of the question of blame or negligence, especially claims under the German Product Liability Act in cases of wilful intent, gross negligence, or injury to life, limb or health and on account of a breach of major contractual obligations. Compensation for failure to comply with major contractual obligations shall be limited, however, to typical, foreseeable damages, unless wilful intent or gross negligence applies, or on account of liability for injury to life, limb or health. Any change in the burden of proof to the detriment of the undersigned customer shall not be associated with the aforesaid rulings

FOR SAFETY REASONS THE CUSTOMER IS NOT ALLOWED TO SELL EVALUATION PRODUCTS TO ANY END CUSTOMER OR ANY OTHER THIRD PARTY.

If the customer fails not to sell the evaluation products to any end customer or any other third party, then the customer shall indemnify SEMIKRON against all claims by the concerned end customer or third party in respect of any loss, damage or injury arising from the aforesaid reason.

#### Please note:

All values in this technical explanation are typical values. Typical values are the average values expected in large quantities and are provided for information purposes only. These values can and do vary in different applications. All operating parameters should be validated by user's technical experts for each application.

#### **Application and Handling Instructions**

- Please provide for static discharge protection during handling. As long as the hybrid driver is not completely assembled, the input terminals have to be short-circuited. Persons working with devices have to wear a grounded bracelet. Any synthetic floor coverings must not be statically chargeable. Even during transportation the input terminals have to be short-circuited using, for example, conductive rubber. Worktables have to be grounded. The same safety requirements apply to MOSFET- and IGBT-modules.
- Any parasitic inductances within the DC-link have to be minimised. Over-voltages may be absorbed by C- or RCD-snubber networks between main terminals for PLUS and MINUS of the power module.
- When first operating a newly developed circuit, SEMIKRON recommends to apply low collector voltage and load current in the beginning and to increase these values gradually, observing the turn-off behaviour of the free-wheeling diode and the turn-off voltage spikes generated across the IGBT. An oscillographic control will be necessary. Additionally, the case temperature of the module has to be monitored. When the circuit works correctly under rated operation conditions, short-circuit testing may be done, starting again with low collector voltage.
- It is important to feed any errors back to the control circuit and to switch off the device immediately in failure events. Repeated turn-on of the IGBT into a short circuit with a high frequency may destroy the device.
- The inputs of the hybrid driver are sensitive to over-voltage. Voltages higher than V<sub>S</sub> +0,3V or below -0,3V may destroy these inputs. Therefore, control signal over-voltages exceeding the above values have to be avoided.
- The connecting leads between hybrid driver and the power module should be as short as possible (max. 20cm), the driver leads should be twisted.

### **Further application support**

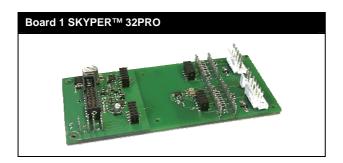
Latest information is available at <a href="http://www.semikron.com">http://www.semikron.com</a>. For design support please read the SEMIKRON Application Manual Power Modules available at <a href="http://www.semikron.com">http://www.semikron.com</a>.

#### **General Description**

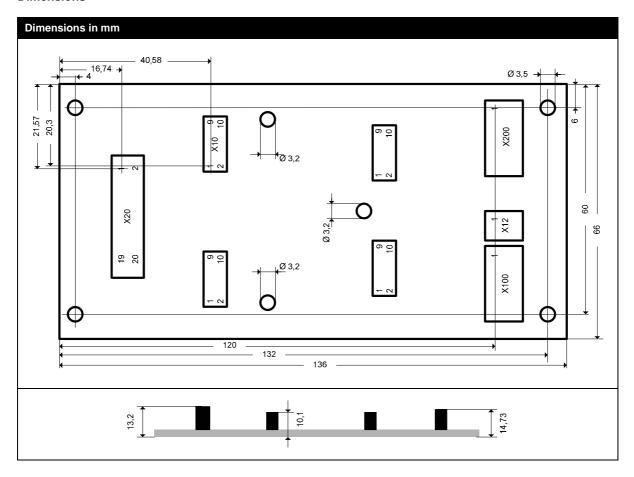
The Board 1 SKYPER $^{\text{TM}}$  32PRO is an evaluation board for the IGBT module e.g. SEMITRANS $^{\text{TM}}$ , SEMiX $^{\text{®}}$  (solder pin version). The board can be customized allowing adaptation and optimization to the used IGBT module. The switching characteristic of the IGBT can be influenced through user settings, e.g. changing turn-on and turn-off speed by variation of  $R_{\text{Gon}}$  and  $R_{\text{Goff}}$ . Furthermore, it is possible to adjust the monitoring level and blanking time for the DSCP, soft turn-off behaviour as well as an over temperature trip level by using the temperature sensor integrated in SEMiX $^{\text{®}}$  modules (see Technical Explanations SKYPER $^{\text{TM}}$  32PRO).

#### Please note:

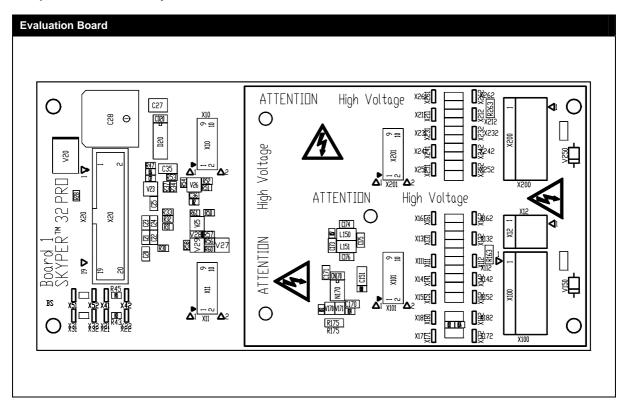
This technical explanation is based on the Technical Explanations for SKYPER $^{\text{TM}}$  32PRO. Please read the Technical Explanations SKYPER $^{\text{TM}}$  32PRO before using the Evaluation Board.



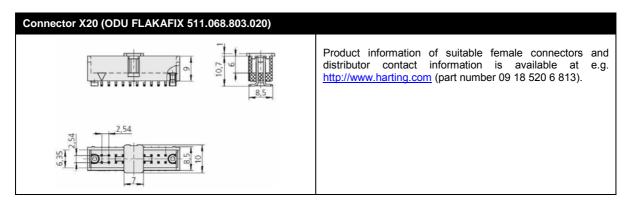
## **Dimensions**



## **Component Placement Layout**

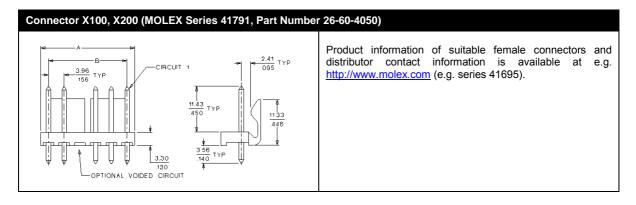


## PIN Array (not SKiiP® compatible)

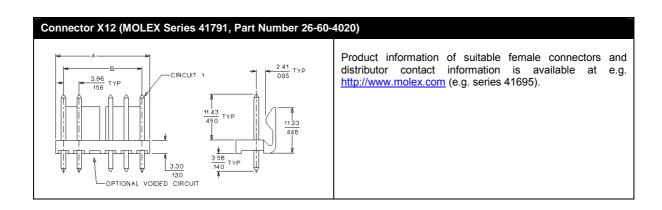


PIN	Signal	Function	Specification
X20:01	IF_PWR_15P	Drive power supply	Stabilised +15V ±4%
X20:02	IF_PWR_GND	GND for power supply	
X20:03	IF_PWR_15P	Drive power supply	Stabilised +15V ±4%
X20:04	IF_PWR_GND	GND for power supply	
X20:05	IF_PWR_15P	Drive power supply	Stabilised +15V ±4%
X20:06	IF_PWR_GND	GND for power supply	
X20:07	reserved		
X20:08	IF_PWR_GND	GND for power supply	
X20:09	IF_CMN_nHALT	Driver core status signal (bidirectional signal with dominant recessive behaviour)	Digital 15V logic; LOW (dominant) = driver disabled; HIGH (recessive) = ready to operate
X20:10	reserved		
X20:11	reserved		
X20:12	IF_CMN_GND	GND for signal IF_CMN_nHALT	
X20:13	reserved		
X20:14	reserved		
X20:15	IF_HB_TOP	Switching signal input (TOP switch)	Digital 15 V logic; 10 kOhm impedance; LOW = TOP switch off; HIGH = TOP switch on
X20:16	IF_HB_BOT	Switching signal input (BOTTOM switch)	Digital 15 V logic; 10 kOhm impedance; LOW = BOT switch off; HIGH = BOT switch on
X20:17	reserved		
X20:18	IF_HB_GND	GND for signals IF_HB_TOP & IF_HB_BOT	
X20:19	reserved		
X20:20	reserved		

### PIN Array - Secondary Side



PIN	Signal	Function	Specification
X100:01	EMITTER_TOP	Emitter output TOP IGBT	
X100:02	reserved		
X100:03	GATE_TOP	Gate output TOP IGBT	
X100:05	VCE_TOP	Collector output TOP IGBT	
X200:01	EMITTER_BOT	Emitter output BOT IGBT	
X200:02	reserved		
X200:03	GATE_BOT	Gate output BOT IGBT	
X200:05	VCE_BOT	Collector output BOT IGBT	

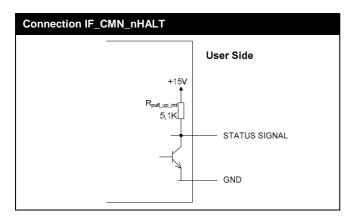


PIN	Signal	Function	Specification
X12:01	SENSE_TEMP_T1	Input temperature signal	NTC + / PTC +
X12:02	SENSE_TEMP_T2	Input temperature signal	NTC - / PTC -

### Signal IF\_CMN\_nHALT

The Halt Logic Signals PRIM\_HALT\_IN and PRIM\_HALT\_OUT of the driver core are coupled to one bidirectional signal (IF\_CMN\_nHALT) with dominant recessive behaviour. IF\_CMN\_nHALT shows the driver core status. When IF\_CMN\_nHALT is HIGH (recessive), the driver core is ready to operate. When IF\_CMN\_nHALT is LOW (dominant), the driver core is disabled / not ready to operate because of e. g. detected failure or driver core system start.

A controller can hold with the IF\_CMN\_nHALT signal the driver core in a safe state (e.g. during a start up of a system or gathered failure signal of other hardware) or generate a coeval release of paralleled driver. Furthermore, paralleled drivers can send and receive IF\_CMN\_nHALT signals among each other by using a single-wire bus.



### **Setting Dead Time**

Designation	Setting	
R43	PRIM_CFG_TDT2_IN	
(connected to GND)	Factory setting: 0Ω	
R44	PRIM_CFG_SELECT_IN	
(connected to GND)	Factory setting: not equipped	
R45	PRIM_CFG_TDT3_IN	
(connected to GND)	Factory setting: 0Ω	
R46	PRIM_CFG_TDT1_IN	
(connected to GND)	Factory setting: not equipped	
actory setting: 4µs		

### **Setting Dynamic Short Circuit Protection**

Setting	
R <sub>CE</sub> Factory setting: not equipped	TOP
C <sub>CE</sub> Factory setting: not equipped	TOP
R <sub>CE</sub> Factory setting: not equipped	ВОТ
C <sub>CE</sub> Factory setting: not equipped	вот
	$\begin{array}{c} R_{\text{CE}} \\ \text{Factory setting: not equipped} \\ \hline C_{\text{CE}} \\ \text{Factory setting: not equipped} \\ \hline R_{\text{CE}} \\ \text{Factory setting: not equipped} \\ \hline C_{\text{CE}} \end{array}$

## **Collector Series Resistance**

Designation	Setting	
R150	R <sub>VCE</sub> * Factory setting: not equipped	ТОР
R250	R <sub>VCE</sub> * Factory setting: not equipped	ВОТ

## **Adaptation Gate Resistors**

Designation	Setting	
R151, R152, R153 (parallel connected)	R <sub>Gon</sub> Factory setting: not equipped	TOF
R154, R155, R156 (parallel connected)	R <sub>Goff</sub> Factory setting: not equipped	TOF
R251, R252, R253 (parallel connected)	R <sub>Gon</sub> Factory setting: not equipped	ВОТ
R254, R255, R256 (parallel connected)	R <sub>Goff</sub> Factory setting: not equipped	ВОТ

# **Setting Soft Turn-Off**

Designation	Setting	
R160, R161 (parallel connected)	R <sub>Goff_SC</sub> Factory setting: not equipped	TOF
R260, R261 (parallel connected)	R <sub>Goff_SC</sub> Factory setting: not equipped	ВОТ

#### **Over Temperature Protection Circuit (OTP)**

The external error input SEC\_TOP\_ERR\_IN on the secondary side (high potential) of the driver core is used for an over temperature protection circuit to place the gate driver into halt mode.

#### **Dimensioning OTP**

If no temperature sensor is connected:

R172: 0Ω (factory setting: not equipped)
 R175: not equip
 R177: not equip
 (factory setting: equipped)
 (factory setting: not equipped)

#### If a NTC temperature sensor is connected:

- 1. Define an over temperature trip level according to the application.
- 2. Calculate the nominal ohmic resistance value of the temperature sensor at the defined trip level according to the IGBT Module explanation.
- The trip level on the adapter board is set with R172 (factory setting: not equipped) by using the calculated resistance value.
- 4. R177 =  $450k\Omega^2 / R_{NTC(@-40^{\circ}C)}[k\Omega]$  (factory setting: not equipped)
- 5. R175: equip (factory setting: equipped)

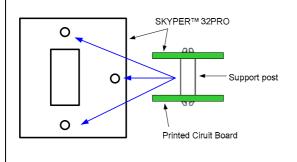
#### If a PTC temperature sensor is connected:

- 1. Define an over temperature trip level according to the application.
- Calculate the nominal ohmic resistance value of the temperature sensor at the defined trip level according to the IGBT Module explanation.
- 3. The trip level on the adapter board is set with R177 =  $450k\Omega^2/R_{calculated\_resistance}[k\Omega]$  (factory setting: not equipped)
- 4. R172 =  $0\Omega$  (factory setting: not equipped)
- 5. R175: equip (factory setting: equipped)

#### **Mounting Notes**

#### **Driver Core Mounting**

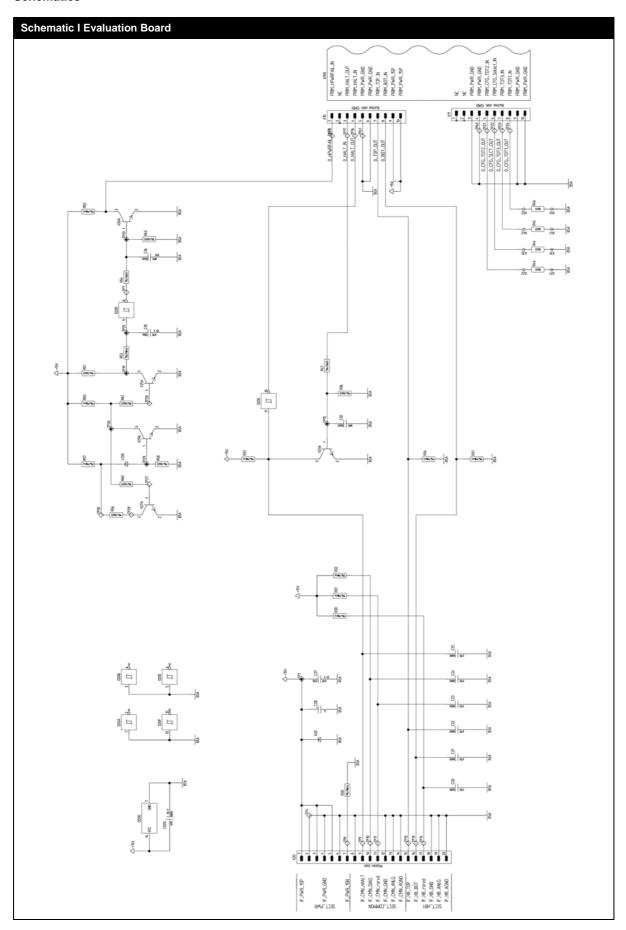
- 1. Soldering of components (e.g. R<sub>Gon</sub>, R<sub>Goff</sub>, etc.) on evaluation board.
- 2. Insert driver core into the box connector on evaluation board.
- 3. The connecting leads between board and power module should be as short as possible (max. 20cm), the leads should be twisted.

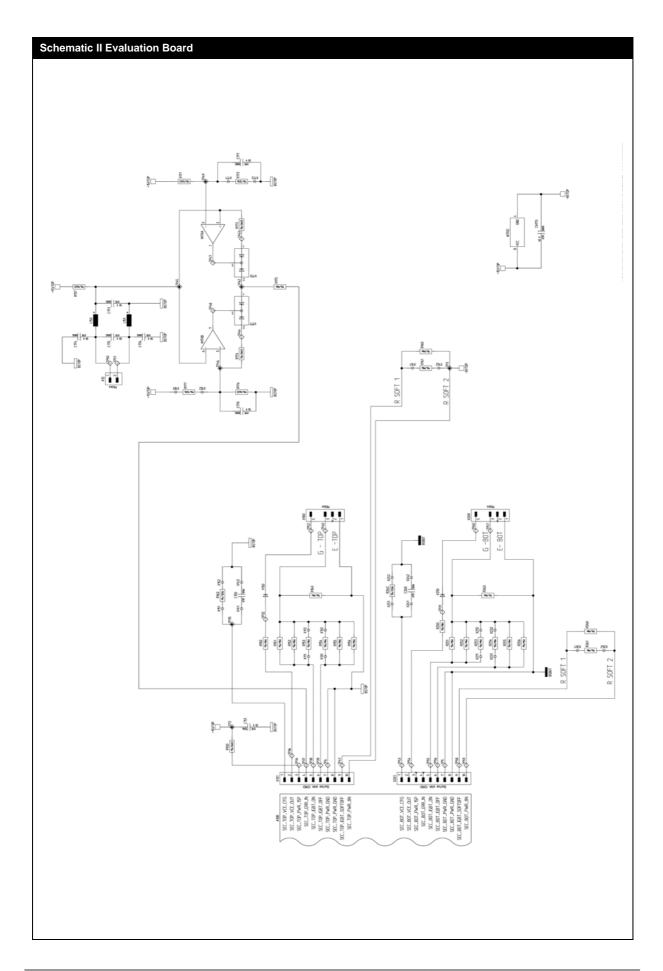


The connection between driver core and evaluation board should be mechanical reinforced by using support posts. The posts have to be spaced between driver core and evaluation board.

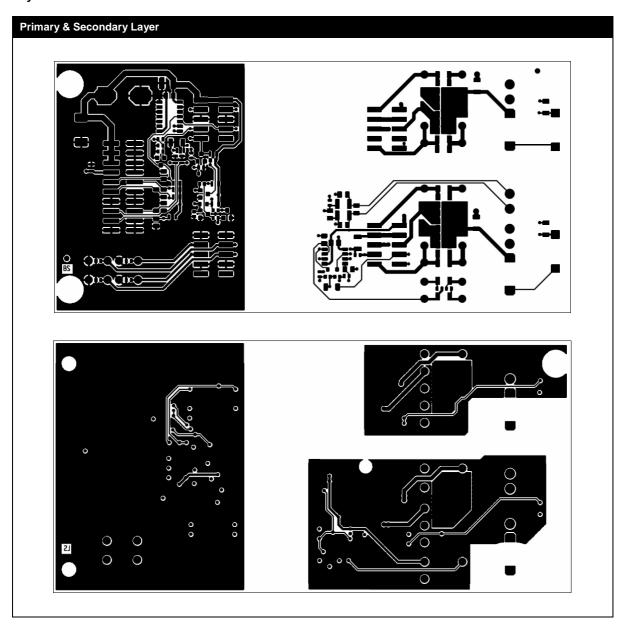
Product information of suitable support posts and distributor contact information is available at e.g. <a href="http://www.richcoinc.com">http://www.richcoinc.com</a> (part number MSPM-8-01).

### **Schematics**





# Layouts



## **Parts List**

## **Parts List Evaluation Board**

Count	Ref. Designator	Value	Pattern Name	Description
8	C170, C171, C173, C174, C175, C176, CD20, CN170,	100nF	0805 (SMD)	Capacitor X7R
6	C20, C21, C22, C23, C24, C25	1nF	0805 (SMD)	Capacitor X7R
1	C27	2,2µF	1210 (SMD)	Capacitor X7R
1	C28	220uF/35V	SMD	Longlife-Elko
1	C32	68pF	0603 (SMD)	Capacitor NP0
2	C35, C151	1uF	1206 (SMD)	Capacitor X7R
1	C36	100pF	0603 (SMD)	Capacitor NP0
1	D20	74C14	SOIC 14 (SMD)	Logic-IC 74C
2	L150, L151	100uH	SIMID02 (SMD)	Inductor
1	N170	LM2904	SOIC 8 (SMD)	Operational Amplifier
1	R100	10,0Ohm	0603 (SMD)	1%
2	R157, R171	15,0KOhm	0603 (SMD)	1%
2	R163, R263	10,0KOhm	MiniMelf (SMD)	1%
3	R173, R174, R176	30,1KOhm	0603 (SMD)	1%
1	R175	5,62KOhm	MiniMelf (SMD)	1%
3	R28, R50, R52	10,0KOhm	MikroMelf (SMD)	1%
6	R30, R31, R32, R33, R34, R37	5,11KOhm	MikroMelf (SMD)	1%
1	R36	3,32KOhm	0603 (SMD)	1%
2	R43, R45	0,00Ohm	0603 (SMD)	
3	R47, R54, R56	10,0KOhm	0603 (SMD)	1%
1	R51	121KOhm	0603 (SMD)	1%
1	R53	100Ohm	MikroMelf (SMD)	1%
1	R57	1,50KOhm	MikroMelf (SMD)	1%
1	R58	1,00KOhm	0603 (SMD)	1%
2	R60, R61	2,00KOhm	0603 (SMD)	1%
1	R62	3,92KOhm	0603 (SMD)	1%
2	V150, V250	BY203/20S		High Voltage Diode
2	V170, V171	BAV70W	SOT323 (SMD)	Double Diode
1	V20	SMCJ15	DO214AB (SMD)	Suppressor Diode
5	V23, V25, V26, V27, V29	BC847B	SOT23 (SMD)	NPN-Transistor
1	V28	BZX284-C7V5	SOD110 (SMD)	Zener-Diode
4	X10, X11, X101, X201	RM2,54 10p.	SMD	Box Connector
2	X100, X200	5p.		Connector
1	X12	2p.		Connector
1	X20	20p.	SMD	Connector

TP: Test Point

Box Connector: SUYIN 254100FA010G200ZU