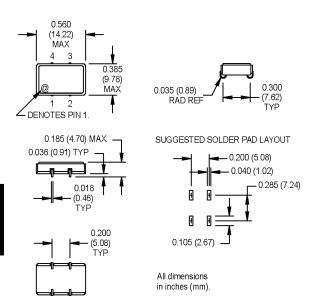
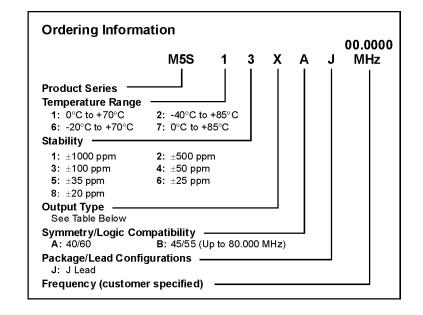
M5S Series 3.3 Volt PECL Oscillators





M5S Series PECL Clock Oscillators with Optional Complementary Outputs. **PLL Version**





Available Stabilities vs. Temperature

TS	1	2	3	4	5	6	8
1	Α	Α	S	Α	Α	C	C
2	Α	Α	Α	Α	Α	С	N
6	Α	Α	Α	Α	Α	С	С
7	Α	Α	Α	Α	Α	С	N

A = Available N = Not Available

S = Standard

C = Consult Factory

Pin Connections

PIN	FUNCTION(S) (Model Dependent)			
1	N/C or Output #2			
2	Case Ground			
3	Output #1			
4	+Vcc			

PECL Output Type

PIN FUNCTIONS:	х	Z
Single Output Pin 3		
Dual Output Pin 1 & 3		٠
Pin 2 Case Ground		۰
Pin 4 +Vcc		

Electrical Specifications

Standard Operating Conditions • 0°C to +70°C; Vcc = 3.3 ±5% VDC							
PARAMETERS	MIN.	TYP.	MAX.	UNITS			
Frequency Range	19.440		100.000	MHz			
Output Load ¹							
Symmetry ²	40/60		60/40	%			
Logic "0" Level			Vcc-1.63	٧			
Logic "1" Level	Vcc-0.98			٧			
Rise/Fall Time ³			2.5	nS			
Supply Current		50	90	mA			

¹ Internally terminated outputs. See load circuit diagram #4 on page 137.

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Symmetry is measured at Vcc-1.3 V level.
Rise/Fall times are measured between Vcc-0.98 and Vcc-1.63. Utilizes PLL clock generation. Not recommended for low jitter applications. See page 136, Figure "2" for suggested solder profile.