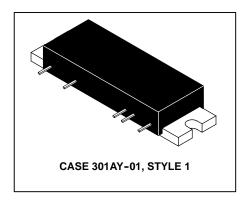
The RF Line PCS Band RF Linear LDMOS Amplifier

Designed for ultra-linear amplifier applications in 50 Ohm systems operating in the PCS frequency band. A silicon FET Class A design provides outstanding linearity and gain. In addition, the excellent group delay and phase linearity characteristics are ideal for digital modulation systems, such as TDMA, EDGE and CDMA.

- · Third Order Intercept Point: 50 dBm Typ
- Power Gain: 29.4 dB Typ (@ f = 1960 MHz)
- Excellent Phase Linearity and Group Delay Characteristics
- · Ideal for Feedforward Base Station Application

MHL19926

1930-1990 MHz, 10 W, 29.4 dB RF LINEAR LDMOS AMPLIFIER



ABSOLUTE MAXIMUM RATINGS (T_C = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
DC Supply Voltage	V_{DD}	30	Vdc
RF Input Power	P _{in}	+17	dBm
Storage Temperature Range	T _{stg}	-40 to +100	°C
Operating Case Temperature Range	T _C	-20 to +100	°C

ELECTRICAL CHARACTERISTICS ($T_C = +25$ °C; $V_{DD} = 26$ Vdc; 50 Ω System)

Character	istic	Symbol	Min	Тур	Max	Unit
Supply Current		I _{DD}	_	1	1.05	А
Power Gain	(f = 1960 MHz)	Gp	28.4	29.4	30.4	dB
Gain Flatness	(f = 1930-1990 MHz)	G _F	_	0.3	0.5	dB
Power Output @ 1 dB Compression	(f = 1960 MHz)	P1 dB	39	40	_	dBm
Input VSWR	(f = 1930-1990 MHz)	VSWR _{in}	_	1.2:1	1.5:1	
Third Order Intercept	(f1 =1957 MHz, f2=1962 MHz)	ITO	49.5	50	_	dBm
Noise Figure	(f = 1990 MHz)	NF	_	4.2	5	dB

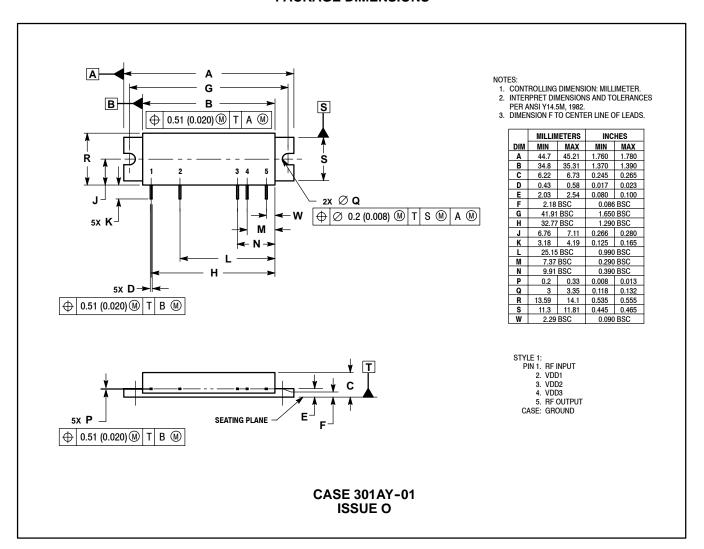
NOTE - <u>CAUTION</u> - MOS devices are susceptible to damage from electrostatic charge. Reasonable precautions in handling and packaging MOS devices should be observed.





Freescale Semiconductor, Inc.

PACKAGE DIMENSIONS



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