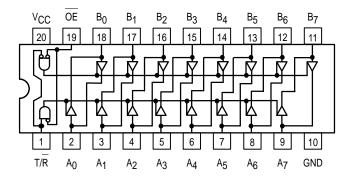


Octal Bidirectional Transceiver with 3-State Inputs/Outputs

The MC74AC245/74ACT245 contains eight non-inverting bidirectional buffers with 3-state outputs and is intended for bus-oriented applications. Current sinking capability is 24 mA at both the A and B ports. The Transmit/Receive (T/R) input determines the direction of data flow through the bidirectional transceiver. Transmit (active-HIGH) enables data from A ports to B ports; Receive (active-LOW) enables data from B ports to A ports. The Output Enable input, when HIGH, disables both A and B ports by placing them in a High Z condition.

- Noninverting Buffers
- Bidirectional Data Path
- A and B Outputs Source/Sink 24 mA
- 'ACT245 Has TTL Compatible Inputs



PIN NAMES

- OE Output Enable Input T/R Transmit/Receive Input
- A₀-A₇ Side A 3-State Inputs or 3-State Outputs
- B₀–B₇ Side B 3-State Inputs or 3-State Outputs

TRUTH TABLES

Inp	uts	Outputs				
OE	T/R	Outputs				
L	L	Bus B Data to Bus A				
L	н	Bus A Data to Bus B				
н	Х	High Z State				

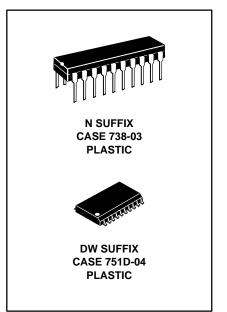
H = HIGH Voltage Level

L = LOW Voltage Level

X = Immaterial

OCTAL BIDIRECTIONAL TRANSCEIVER WITH 3-STATE INPUTS/OUTPUTS

MC74AC245 MC74ACT245



MAXIMUM RATINGS*

Symbol	Parameter	Value	Unit
VCC	DC Supply Voltage (Referenced to GND)	-0.5 to +7.0	V
V _{in}	DC Input Voltage (Referenced to GND)	–0.5 to V _{CC} +0.5	V
V _{out}	DC Output Voltage (Referenced to GND)	–0.5 to V _{CC} +0.5	V
l _{in}	DC Input Current, per Pin	±20	mA
l _{out}	DC Output Sink/Source Current, per Pin	±50	mA
ICC	DC V _{CC} or GND Current per Output Pin	±50	mA
T _{stg}	Storage Temperature	-65 to +150	°C

* Maximum Ratings are those values beyond which damage to the device may occur. Functional operation should be restricted to the Recommended Operating Conditions.

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter		Min	Тур	Max	Unit
Maa	Supply Voltogo	ΆC	2.0	5.0	6.0	V
Vcc	Supply Voltage	Ϋ́ACT	4.5	5.0	5.5	V
V _{in} , V _{out}	DC Input Voltage, Output Voltage (Ref. to GND)		0		VCC	V
		V _{CC} @ 3.0 V		150		
t _r , t _f	Input Rise and Fall Time (Note 1) 'AC Devices except Schmitt Inputs	V _{CC} @ 4.5 V		40		ns/V
		V _{CC} @ 5.5 V		25		
	Input Rise and Fall Time (Note 2)	V _{CC} @ 4.5 V		10		ns/V
t _r , t _f	'ACT Devices except Schmitt Inputs	V _{CC} @ 5.5 V		8.0		115/ V
Тj	Junction Temperature (PDIP)				140	°C
T _A	Operating Ambient Temperature Range		-40	25	85	°C
IOH	Output Current — High				-24	mA
IOL	Output Current — Low				24	mA

1. V_{in} from 30% to 70% V_{CC} ; see individual Data Sheets for devices that differ from the typical input rise and fall times. 2. V_{in} from 0.8 V to 2.0 V; see individual Data Sheets for devices that differ from the typical input rise and fall times.

DC CHARACTERISTICS

			74	AC	74AC			
Symbol	Parameter	V _{CC} (V)	T _A = +25°C		T _A = −40°C to +85°C	Unit	Conditions	
			Тур	Guar	anteed Limits			
VIH	Minimum High Level Input Voltage	3.0 4.5 5.5	1.5 2.25 2.75	2.1 3.15 3.85	2.1 3.15 3.85	V	$V_{OUT} = 0.1 V$ or $V_{CC} - 0.1 V$	
VIL	Maximum Low Level Input Voltage	3.0 4.5 5.5	1.5 2.25 2.75	0.9 1.35 1.65	0.9 1.35 1.65	V	$V_{OUT} = 0.1 V$ or $V_{CC} - 0.1 V$	
V _{OH}	Minimum High Level Output Voltage	3.0 4.5 5.5	2.99 4.49 5.49	2.9 4.4 5.4	2.9 4.4 5.4	V	I _{OUT} = -50 μA	
		3.0 4.5 5.5		2.56 3.86 4.86	2.46 3.76 4.76	V	*VIN = VIL or VIH -12 mA IOH -24 mA -24 mA	
V _{OL}	Maximum Low Level Output Voltage	3.0 4.5 5.5	0.002 0.001 0.001	0.1 0.1 0.1	0.1 0.1 0.1	V	I _{OUT} = 50 μA	
		3.0 4.5 5.5		0.36 0.36 0.36	0.44 0.44 0.44	V	*VIN = VIL or VIH 12 mA IOL 24 mA 24 mA	
I _{IN}	Maximum Input Leakage Current	5.5		±0.1	±1.0	μΑ	$V_1 = V_{CC}, GND$	
IOZT	Maximum 3-State Current	5.5		±0.6	±6.0	μΑ	$V_{I} (OE) = V_{IL}, V_{IH}$ $V_{I} = V_{CC}, GND$ $V_{O} = V_{CC}, GND$	
I _{OLD}	†Minimum Dynamic	5.5			75	mA	V _{OLD} = 1.65 V Max	
IOHD	Output Current	5.5			-75	mA	V _{OHD} = 3.85 V Min	
ICC	Maximum Quiescent Supply Current	5.5		8.0	80.0	μA	$V_{IN} = V_{CC}$ or GND	

* All outputs loaded; thresholds on input associated with output under test. †Maximum test duration 2.0 ms, one output loaded at a time.

Note: I_{IN} and I_{CC} @ 3.0 V are guaranteed to be less than or equal to the respective limit @ 5.5 V V_{CC}.

	Parameter	Vcc* (V)	74AC T _A = +25°C C _L = 50 pF			74AC T _A = -40°C to +85°C C _L = 50 pF		Unit	Fig. No.
Symbol									
			Min	Тур	Max	Min	Max		
^t PLH	Propagation Delay A _n to B _n or B _n to A _n	3.3 5.0	1.5 1.5	5.0 3.5	8.5 6.5	1.0 1.0	9.0 7.0	ns	3-5
^t PHL	Propagation Delay A _n to B _n or B _n to A _n	3.3 5.0	1.5 1.5	5.0 3.5	8.5 6.0	1.0 1.0	9.0 7.0	ns	3-5
^t PZH	Output Enable Time	3.3 5.0	2.5 1.5	7.0 5.0	11.5 8.5	2.0 1.0	12.5 9.0	ns	3-7
^t PZL	Output Enable Time	3.3 5.0	2.5 1.5	7.5 5.5	12.0 9.0	2.0 1.0	13.5 9.5	ns	3-8
^t PHZ	Output Disable Time	3.3 5.0	2.0 1.5	6.5 5.5	12.0 9.0	1.0 1.0	12.5 10.0	ns	3-7
^t PLZ	Output Disable Time	3.3 5.0	2.0 1.5	7.0 5.5	11.5 9.0	1.5 1.0	13.0 10.0	ns	3-8

AC CHARACTERISTICS (For Figures and Waveforms — See Section 3)

 * Voltage Range 3.3 V is 3.3 V ± 0.3 V. Voltage Range 5.0 V is 5.0 V ± 0.5 V.

DC CHARACTERISTICS

			74ACT T _A = +25°C		74ACT	Unit		
Symbol	Parameter	V _{CC} (V)			T _A = −40°C to +85°C		Conditions	
			Тур	Guar	anteed Limits			
VIH	Minimum High Level Input Voltage	4.5 5.5	1.5 1.5	2.0 2.0	2.0 2.0	V	V _{OUT} = 0.1 V or V _{CC} – 0.1 V	
VIL	Maximum Low Level Input Voltage	4.5 5.5	1.5 1.5	0.8 0.8	0.8 0.8	V	V _{OUT} = 0.1 V or V _{CC} – 0.1 V	
Vон	Minimum High Level Output Voltage	4.5 5.5	4.49 5.49	4.4 5.4	4.4 5.4	V	I _{OUT} = -50 μA	
		4.5 5.5		3.86 4.86	3.76 4.76	V	*V _{IN} = V _{IL} or V _{IH} -24 mA IOH -24 mA	
VOL	Maximum Low Level Output Voltage	4.5 5.5	0.001 0.001	0.1 0.1	0.1 0.1	V	I _{OUT} = 50 μA	
		4.5 5.5		0.36 0.36	0.44 0.44	V	*VIN = VIL or VIH 24 mA IOL 24 mA	
I _{IN}	Maximum Input Leakage Current	5.5		±0.1	±1.0	μA	$V_1 = V_{CC}, GND$	
∆ICCT	Additional Max. ICC/Input	5.5	0.6		1.5	mA	$V_{I} = V_{CC} - 2.1 V$	
IOZT	Maximum 3-State Current	5.5		±0.6	±6.0	μΑ		
IOLD	†Minimum Dynamic	5.5			75	mA	V _{OLD} = 1.65 V Max	
IOHD	Output Current	5.5			-75	mA	V _{OHD} = 3.85 V Min	
ICC	Maximum Quiescent Supply Current	5.5		8.0	80.0	μA	$V_{IN} = V_{CC}$ or GND	

* All outputs loaded; thresholds on input associated with output under test. † Maximum test duration 2.0 ms, one output loaded at a time.

	Parameter	V _{CC} * (V)	74ACT T _A = +25°C C _L = 50 pF			74ACT		Unit	Fig. No.
Symbol						T _A = -40°C to +85°C C _L = 50 pF			
			Min	Тур	Max	Min	Max		
^t PLH	Propagation Delay A _n to B _n or B _n to A _n	5.0	1.5	4.0	7.5	1.5	8.0	ns	3-5
^t PHL	Propagation Delay A _n to B _n or B _n to A _n	5.0	1.5	4.0	8.0	1.0	9.0	ns	3-5
^t PZH	Output Enable Time	5.0	1.5	5.0	10	1.5	11.0	ns	3-7
t _{PZL}	Output Enable Time	5.0	1.5	5.5	10	1.5	12.0	ns	3-8
^t PHZ	Output Disable Time	5.0	1.5	5.5	10	1.0	11.0	ns	3-7
^t PLZ	Output Disable Time	5.0	2.0	5.0	10	1.5	11.0	ns	3-8

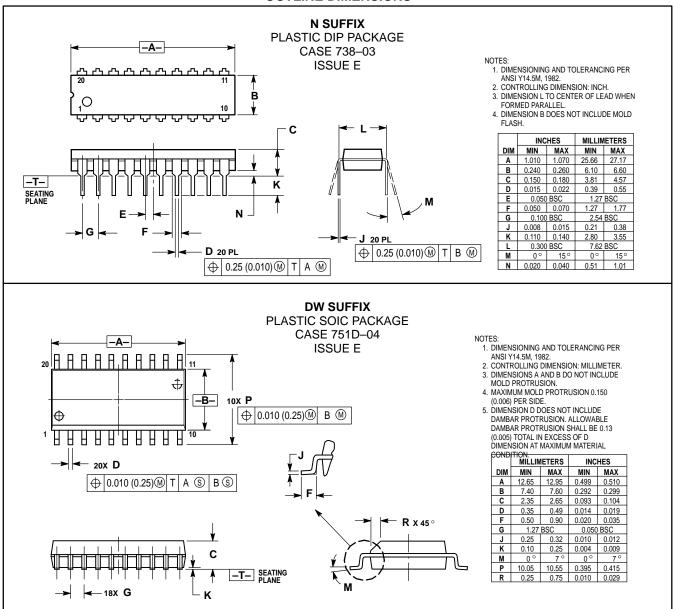
AC CHARACTERISTICS (For Figures and Waveforms — See Section 3)

* Voltage Range 5.0 V is 5.0 V ± 0.5 V.

CAPACITANCE

Symbol	Parameter	Value Typ	Unit	Test Conditions
C _{IN}	Input Capacitance	4.5	pF	V _{CC} = 5.0 V
C _{I/O}	Input/Output Capacitance	15	pF	V _{CC} = 5.0 V
C _{PD}	Power Dissipation Capacitance	45	pF	V _{CC} = 5.0 V

OUTLINE DIMENSIONS



Motorola reserves the right to make changes without further notice to any products herein. Motorola makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does Motorola assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters can and do vary in different applications. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. Motorola does not convey any license under its patent rights nor the rights of others. Motorola products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the Motorola product could create a situation where personal injury or death may occur. Should Buyer purchase or use Motorola products for any such unintended or unauthorized application, Buyer shall indemnify and hold Motorola and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Motorola was negligent regarding the design or manufacture of the part. Motorola and was negligent regarding the design or manufacture of the part.

How to reach us:

USA/EUROPE: Motorola Literature Distribution; P.O. Box 20912; Phoenix, Arizona 85036. 1–800–441–2447 JAPAN: Nippon Motorola Ltd.; Tatsumi–SPD–JLDC, Toshikatsu Otsuki, 6F Seibu–Butsuryu–Center, 3–14–2 Tatsumi Koto–Ku, Tokyo 135, Japan. 03–3521–8315

MFAX: RMFAX0@email.sps.mot.com -TOUCHTONE (602) 244-6609 INTERNET: http://Design-NET.com

0

HONG KONG: Motorola Semiconductors H.K. Ltd.; 8B Tai Ping Industrial Park, 51 Ting Kok Road, Tai Po, N.T., Hong Kong. 852–26629298



