520MHz Dual Modulus Prescaler

The MC12025 is a dual modulus prescaler which divides by 64 and 65. Supply voltages of 4.75 to 5.25V may be connected to Pin 8.

- 520MHz Toggle Frequency
- Low–Power 9.5mA Typical
- Control Input Is Compatible WIth Standard CMOS and TTL
- Operating Supply Voltage of 5.0V ±0.25V
- Propagation Delay 30ns Typical

MAXIMUM RATINGS

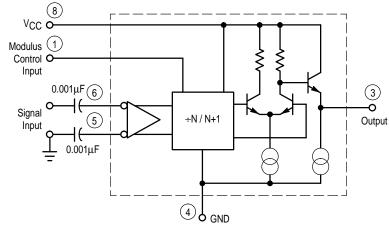
Symbol	Characteristic	Range	Unit
V _{CC}	Power Supply Voltage, Pin 8	-0.5 to 7.0	Vdc
TA	Operating Temperature Range	-40 to +85	°C
T _{stg}	Storage Temperature Range	-65 to +175	°C

ELECTRICAL CHARACTERISTICS ($V_{CC} = 4.75$ to 5.25V; $T_A = -40$ to +85°C)

		+85°C)			
Symbol	Characteristic	Min	Тур	Max	Unit
^f max ^f min	Toggle Frequency (Sine Wave Input)	520		30	MHz
ICC	Supply Current		9.5	13.5	mA
VIH	Control Input HIGH (+64)	2.0			V
VIH	Control Input LOW (÷65)			0.8	V
Vout	Output Voltage	0.8	1.2		VPP
V _{in}	Input Voltage Sensitivity 30MHz 100–520MHz	400 100		800 800	тVрр
tPLL	PLL Response Time ¹			t _{out} -422	ns

1. tpLL = The period of time the PLL has from the rising output transition to the Modulus Control input edge transition to ensure proper modulus selection

2. tout = Period of output waveform



PRESCALER BLOCK DIAGRAM

MC12025

MECL PLL COMPONENTS

÷64/65 DUAL MODULUS PRESCALER

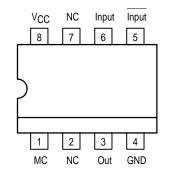


P SUFFIX 8–LEAD PLASTIC PACKAGE CASE 626–05



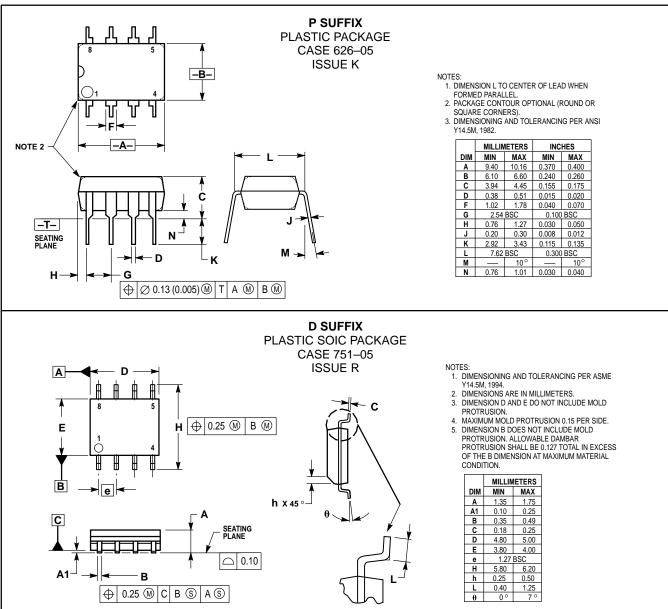
D SUFFIX 8–LEAD PLASTIC SOIC PACKAGE CASE 751–05

Pinout: 8-Lead Plastic (Top View)





OUTLINE DIMENSIONS



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