

RS232 Driver/Receiver with Shutdown

FEATURES

- Operates from $\pm 5V$ to $\pm 15V$ Supplies
- Fully Protected Against Overload
- Outputs Can Be Driven ±30V Without Damage
- Three-State Outputs: Outputs Open When Off
- Bipolar Circuit—No Latchup
- ±30V Input Range
- Triple Driver/Receiver
- No Supply Current in Shutdown
- $30k\Omega$ Input Impedance
- Meets All RS232 Specifications
- 16-Pin Version—Pin Compatible with MC145406
- Available in SO Package
- 15kV ESD: LT1039A

APPLICATIONS

- RS232 Interface
- Terminals
- Modems

DESCRIPTION

The LT®1039 is a triple RS232 driver/receiver that includes shutdown. Each receiver will accept up to $\pm 30V$ input and can drive either TTL or CMOS logic. The RS232 drivers accept TTL logic inputs and output RS232 voltage levels. The outputs are fully protected against overload and can be shorted to ground or up to ±30V without damage to the drivers. Additionally, when the system is shut down or power is off, the outputs are in a high impedance state allowing data line sharing. Bipolar circuitry makes this driver/receiver exceptionally rugged against overloads or ESD damage.

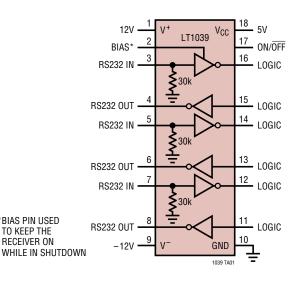
A BIAS pin allows one receiver to be kept on while the rest of the part is shut down.

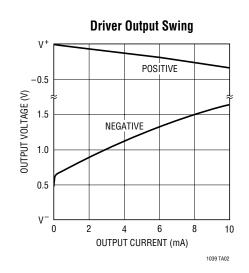
The LT1039 is also available in the 16-pin version, without shutdown or BIAS pin functions.

For applications requiring operation from a single 5V supply, see the LT1080A/LT1081A, LT1180A/LT1181A and LT1130A data sheets. For applications requiring 15kV ESD, see the LT1039A.

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TYPICAL APPLICATION





*BIAS PIN USED

TO KEEP THE

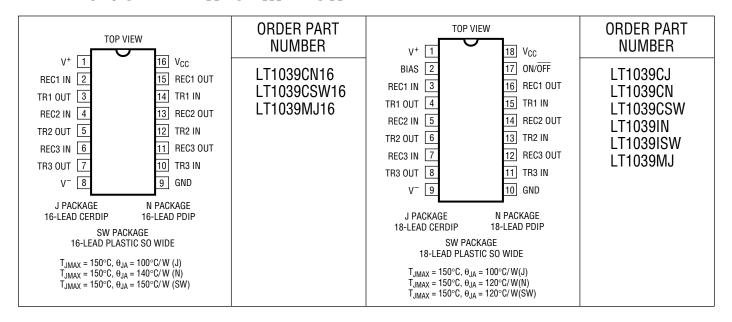
RECEIVER ON

ABSOLUTE MAXIMUM RATINGS (Note 1)

Supply Voltage	
Driver (V+, V-)	±16V
Receiver (V _{CC})	7V
Logic Inputs	
Receiver Inputs	±30V
ON/OFF Input	GND to 12V
Driver Outputs	$(V^- + 30V)$ to $(V^+ - 30V)$

Short-Circuit Duration	Indefinite
Operating Temperature Range	
LT1039C	0°C to 70°C
LT1039I	40°C to 85°C
LT1039M	55°C to 125°C
Storage Temperature Range	– 65°C to 150°C
Lead Temperature (Soldering, 10 sec)300°C

PACKAGE/ORDER INFORMATION



ELECTRICAL CHARACTERISTICS

The \bullet denotes specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25^{\circ}C$.

PARAMETER	CONDITIONS	CONDITIONS		TYP	MAX	UNITS
Driver V ⁺ = 12V, V ⁻ = -12V, V _{ON/OFF} = 2.5V (Note 2)						
Output Voltage Swing	Load = 3k to Ground Positive Negative	•	V+ - 0.4 V- + 1.5	V+ - 0.1 V- + 1.0		V
Logic Input Voltage Levels	Input Low Level (V _{OUT} = High) Input High Level (V _{OUT} = Low)	•	2.0	1.4 1.4	0.8	V
Logic Input Current	$V_{IN} \ge 2.0V$ $V_{IN} \le 0.8V$			1 5	20 20	μA μA
Output Short-Circuit Current	Sourcing Current, V _{OUT} = 0V Sinking Current, V _{OUT} = 0V		5 -5	15 -15		mA mA
Output Leakage Current	Shutdown (Notes 3, 4), V _{OUT} = ±18V, V _{IN} = 0V	•		10 (25°C)	200	μА
Supply Leakage Current	Shutdown (Note 3)	•		1 (25°C)	100	μА
Slew Rate	$R_L = 3k$, $C_L = 51pF$		4	15	30	V/µs
Supply Current	V _{OUT} = Low			4	8	mA



ELECTRICAL CHARACTERISTICS

The \bullet denotes specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25^{\circ}C$.

PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
Receiver $V_{CC} = 5V$, $V_{ON/\overline{OFF}} = 2.5V$	Receiver $V_{CC} = 5V$, $V_{ON/OFF} = 2.5V$ (Note 2)					
Input Voltage Thresholds	Input Low Level (V _{OUT} = High) Input High Level (V _{OUT} = Low)	•	0.5	1.3 1.7	2.8	V
Hysteresis		•	0.1	0.4	1.0	V
Input Resistance		•		30		kΩ
Output Voltage	Output Low, I _{OUT} = -1.6mA Output High, I _{OUT} = 160µA	•	3.5	0.4 4.8	0.5	V
Output Short-Circuit Current	Sourcing Current, V _{OUT} = 0V (Note 5) Sinking Current, V _{OUT} = V _{CC}	•	0.5 -10	1		mA mA
Output Leakage Current	Shutdown (Note 2), $0V \le V_{OUT} \le V_{CC}$, $V_{IN} = 0V$	•		1	10	μА
Supply Current		•		4	7	mA
Supply Leakage Current	Shutdown (Note 3)	•		1 (25°C)	100	μА
ON/OFF Pin Current	$0V \le V_{ON/\overline{OFF}} \le 5V$	•	-15		80	μА

Note 1: Absolute Maximum Ratings are those values beyond which the life of a device by be impaired.

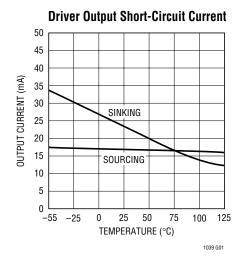
Note 4: For $T_A \ge 100^{\circ}C$, leakage current is $350\mu A$ max. Note 5: For $T_A \le -25^{\circ}C$, output source current is 0.4mA.

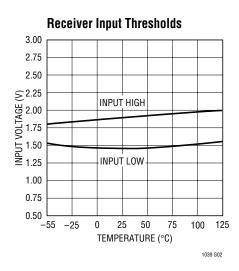
Note 2: $V_{ON/\overline{OFF}} = 5V$ for LT1039M grade devices.

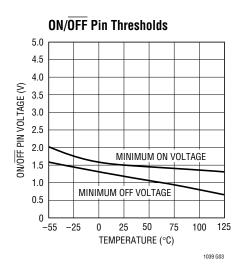
Note 3: $V_{ON/\overline{OFF}} = 0.4V$ for $-55^{\circ}C \le T_A \le 100^{\circ}C$ and $V_{ON/\overline{OFF}} = 0.2V$ for

 $100^{\circ}\text{C} \le T_{A} \le 125^{\circ}\text{C}$. Does not apply to LT1039-16 part.

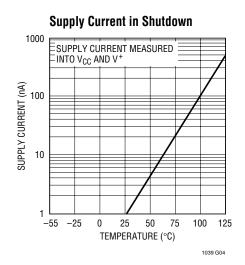
TYPICAL PERFORMANCE CHARACTERISTICS

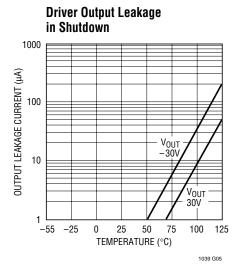


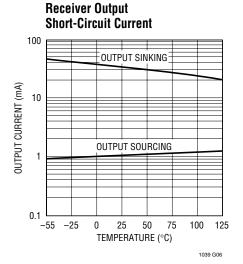


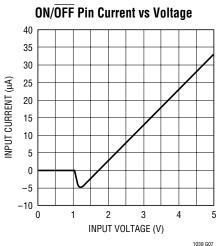


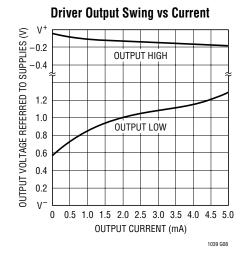
TYPICAL PERFORMANCE CHARACTERISTICS

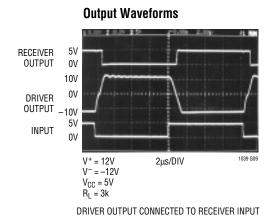


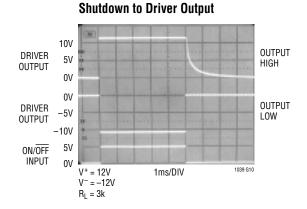








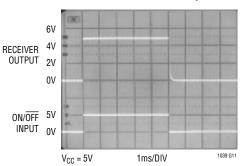




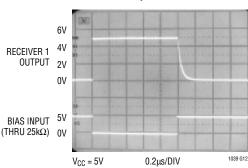
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TYPICAL PERFORMANCE CHARACTERISTICS

Shutdown to Receiver Output



BIAS Pin Response Time



PIN FUNCTIONS (Pin numbers listed are for 18-pin device)

 V^+ , V^- (Pins 1, 9): Driver Supply Pins. Supply current drops to zero in shutdown mode. Driver outputs are in a high impedance state when V^+ and $V^- = 0V$.

BIAS (Pin 2): Keeps receiver 1 on while the LT1039 is in the shutdown mode. Leave BIAS pin open when not in use. See Applications Information for proper use.

REC IN (Pins 3, 5, 7): Receiver Input Pins. Accepts RS232 voltage levels $(\pm 30V)$ and has 0.4V of hysteresis to provide noise immunity. Input impedance is nominally $30k\Omega$.

TR OUT (Pins 4, 6, 8): Driver Outputs with RS232 Voltage Levels. Outputs are in a high impedance state when in the shutdown mode or when power is off (V⁺ and V⁻ = 0V) to allow data line sharing. Outputs are fully short-circuit protected from V⁻ + 30V to V⁺ – 30V with power on, off or in the shutdown mode. Typical output breakdowns are greater than ± 45 V and higher applied voltages will not damage the device if moderately current limited.

GND (Pin 10): Ground Pin.

TR IN (Pins 11, 13, 15): RS232 Driver Input Pins. Inputs are TTL/CMOS compatible. Inputs should not be allowed to float. Tie unused inputs to V_{CC} .

REC OUT (Pins 12, 14, 16): Receiver Outputs with TTL/CMOS Voltage Levels. Outputs are in a high impedance state when in the shutdown mode to allow data line sharing. Outputs are fully short-circuit protected to ground or V_{CC} with power on, off or in the shutdown mode.

ON/OFF (Pin 17): Controls the operation mode of the LT1039 and is TTL/CMOS compatible. A logic low puts the device in the shutdown mode which reduces input supply current to zero and places both driver and receiver outputs in a high impedance state.

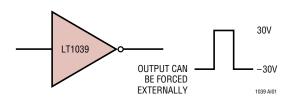
V_{CC} (**Pin 18**): 5V Power for Receivers.



APPLICATIONS INFORMATION

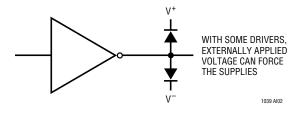
The driver output stage of the LT1039 offers significantly improved protection over older bipolar and CMOS designs. In addition to current limiting, the driver output can be externally forced to $\pm 30V$ with no damage or excessive current flow, and will not disrupt the supplies.

LT1039 Driver



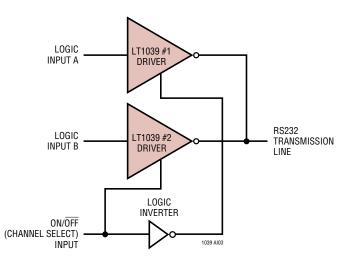
Some drivers have diodes connected between the outputs and the supplies, so externally applied voltages can cause excessive supply voltage to develop

Older RS232 Drivers and Other CMOS Drivers

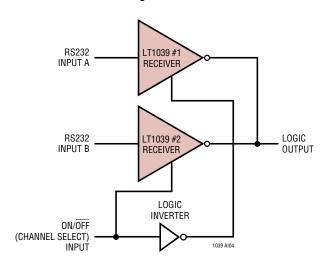


Placing the LT1039 in the shutdown mode (Pin 17 low) puts both the driver and receiver outputs in a high impedance state. This allows data line sharing and transceiver applications.

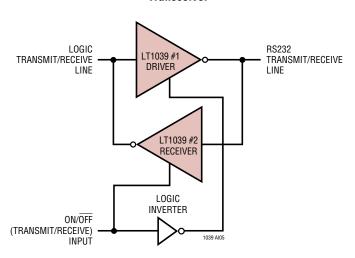
Sharing a Transmitter Line



Sharing a Receiver Line



Transceiver



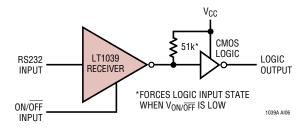
The shutdown mode also drops all supply currents (V_{CC} , V^+ , V^-) to zero for power conscious systems.



APPLICATIONS INFORMATION

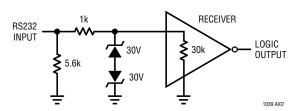
When driving CMOS logic from a receiver that will be used in the shutdown mode and there is no other active receiver on the line, a 51k resistor can be placed from the logic input to V_{CC} to force a definite logic level when the receiver output is in a high impedance state.

Driving CMOS Logic from a Receiver



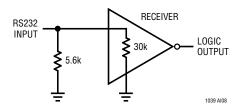
To protect against receiver input overloads in excess of $\pm 30V$, a voltage clamp can be placed on the data line and still maintain RS232 compatibility.

Input Overvoltage Protection



The receiver input impedance of the LT1039 is nominally $30k\Omega$. For applications requiring a $5k\Omega$ input impedance, a 5.6k resistor can be connected from the receiver input to ground.

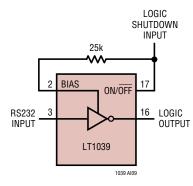
 $5k\Omega$ Impedance Matching



Driver inputs should not be allowed to float. Any unused inputs should be tied to V_{CC} .

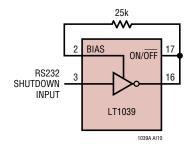
The BIAS pin is used to "keep alive" one receiver while in the shutdown mode (all other circuitry being inactive). This allows a system to be in shutdown and still have one active receiver for transferring data.

Keeping Alive One Receiver While in Shutdown



It can also be used to make an RS232 compatible shutdown control line. Driving the BIAS pin low through a resistance of 24k to 30k keeps the receiver active. Do not drive the BIAS pin directly from a logic output without the series resistor. An unused BIAS pin should be left open.

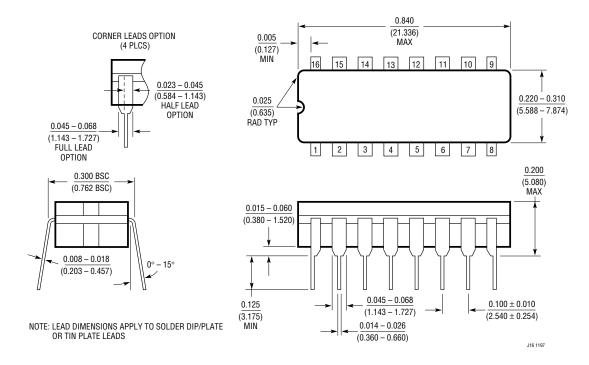
RS232 Compatible Shutdown Control Line





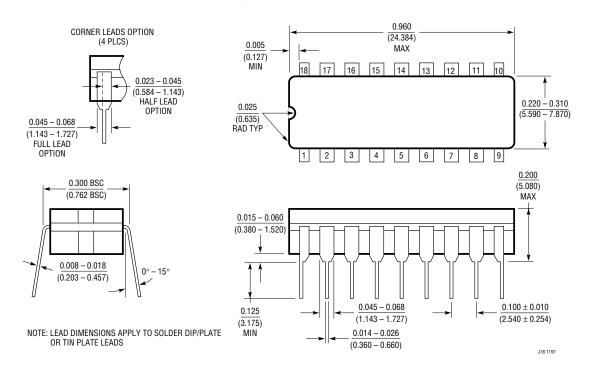
J Package 16-Lead CERDIP (Narrow 0.300, Hermetic)

(LTC DWG # 05-08-1110)



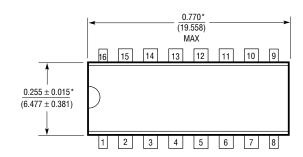
J Package 18-Lead CERDIP (Narrow 0.300, Hermetic)

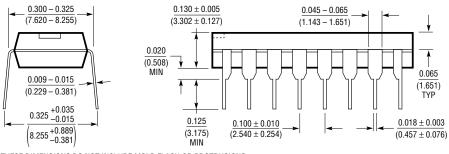
(LTC DWG # 05-08-1110)



N Package 16-Lead PDIP (Narrow 0.300)

(LTC DWG # 05-08-1510)



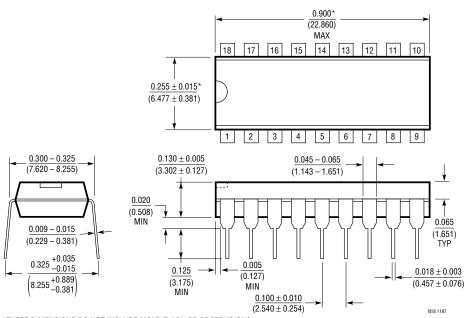


*THESE DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS. MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.010 INCH (0.254mm)

N16 1197

N Package 18-Lead PDIP (Narrow 0.300)

(LTC DWG # 05-08-1510)

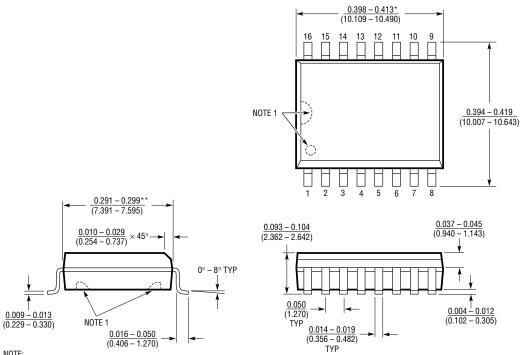


*THESE DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS. MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.010 INCH (0.254mm)



SW Package 16-Lead Plastic Small Outline (Wide 0.300)

(LTC DWG # 05-08-1620)



NOTE:

1. PIN 1 IDENT, NOTCH ON TOP AND CAVITIES ON THE BOTTOM OF PACKAGES ARE THE MANUFACTURING OPTIONS.
THE PART MAY BE SUPPLIED WITH OR WITHOUT ANY OF THE OPTIONS

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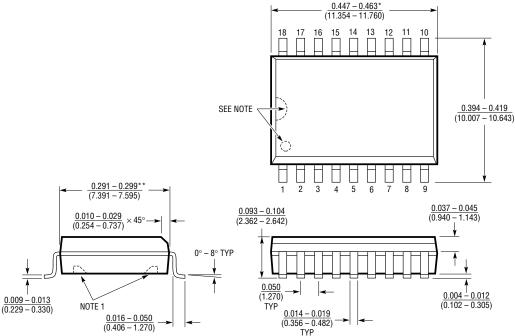
1. PIN 1 IDENT, NOT

S16 (WIDE) 0396

- *DIMENSION DOES NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.006" (0.152mm) PER SIDE
- **DIMENSION DOES NOT INCLUDE INTERLEAD FLASH. INTERLEAD FLASH SHALL NOT EXCEED 0.010" (0.254mm) PER SIDE

SW Package 18-Lead Plastic Small Outline (Wide 0.300)

(LTC DWG # 05-08-1620)



NOTE:

PIN 1 IDENT, NOTCH ON TOP AND CAVITIES ON THE BOTTOM OF PACKAGES ARE THE MANUFACTURING OPTIONS.
THE PART MAY BE SUPPLIED WITH OR WITHOUT ANY OF THE OPTIONS

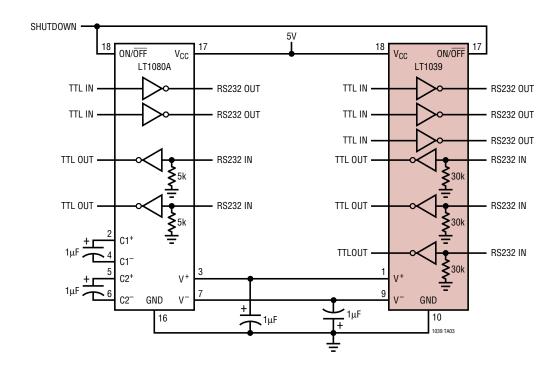
S18 (WIDE) 0396

*DIMENSION DOES NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.006" (0.152mm) PER SIDE

**DIMENSION DOES NOT INCLUDE INTERLEAD FLASH. INTERLEAD FLASH SHALL NOT EXCEED 0.010" (0.254mm) PER SIDE

TYPICAL APPLICATION

LT1080A (Driver/Receiver with Power Supply) Driving an LT1039



RELATED PARTS

PART NUMBER	DESCRIPTION	COMMENTS
LTC®485	Low Power RS485 Transceiver	Industry Standard
LT1039A/LT1039A-16	Triple RS232 Driver/Receiver with Shutdown	±15kV ESD Protection
LT1137A	5V 3-Driver/5-Receiver RS232 Transceiver	±15kV ESD Protection
LT1180A/LT1181A	5V 2-Driver/2-Receiver RS232 Transceiver	Industry Standard, 0.1µF Capacitors
LTC1345	V.35 Transceiver	Single 5V Supply Using 0.1µF Capacitors
LTC1348	3.3V 3-Driver/5-Receiver RS232 Transceiver	Operates from 3.3V to 5V Supplies, 5 Receivers Active in Shutdown