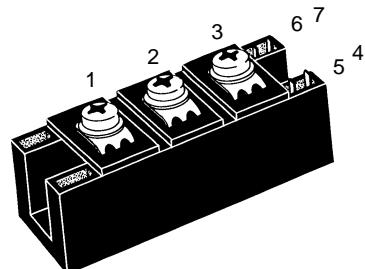
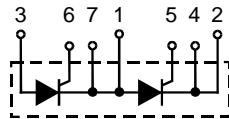


Thyristor Module

Preliminary data

I_{TRMS} = 2x300 A
I_{TAVM} = 2x128 A
V_{RRM, DRM} = 800-1800 V

V _{RSM}	V _{RRM}	Type
V _{DSM}	V _{DRM}	
V	V	
900	800	MCC 122-08io1
1300	1200	MCC 122-12io1
1500	1400	MCC 122-14io1
1700	1600	MCC 122-16io1
1900	1800	MCC 122-18io1



Symbol	Conditions	Maximum Ratings		
I _{TRMS}		300	A	
I _{TAVM}	T _C = 85°C; 180° sine	128	A	
I _{TSM}	T _{VJ} = 45°C V _R = 0	t = 10 ms (50 Hz), sine t = 8.3 ms (60 Hz), sine	3600 3850	A A
	T _{VJ} = T _{VJM} V _R = 0	t = 10 ms (50 Hz), sine t = 8.3 ms (60 Hz), sine	3200 3420	A A
I ² dt	T _{VJ} = 45°C V _R = 0	t = 10 ms (50 Hz), sine t = 8.3 ms (60 Hz), sine	64800 62300	A ² s A ² s
	T _{VJ} = T _{VJM} V _R = 0	t = 10 ms (50 Hz), sine t = 8.3 ms (60 Hz), sine	51200 49100	A ² s A ² s
(di/dt) _{cr}	T _{VJ} = T _{VJM} f = 50Hz, t _p = 200μs V _D = 2/3 V _{DRM} I _G = 0.5 A di _G /dt = 0.5 A/μs	repetitive, I _T = 500 A non repetitive, I _T = 500 A	150 500	A/μs A/μs
(dv/dt) _{cr}	T _{VJ} = T _{VJM} ; V _{DR} = 2/3 V _{DRM} R _{GR} = ∞; method 1 (linear voltage rise)		1000	V/μs
P _{GM}	T _{VJ} = T _{VJM} I _T = I _{TAVM}	t _p = 30 μs t _p = 500 μs	120 60	W W
P _{GAV}			8	W
V _{RGM}			10	V
T _{VJ}			-40...+125	°C
T _{VJM}			125	°C
T _{stg}			-40...+125	°C
V _{ISOL}	50/60 Hz, RMS I _{ISOL} ≤ 1 mA	t = 1 min t = 1 s	3000 3600	V~ V~
M _d	Mounting torque (M6) Terminal connection torque (M6)		2.25-2.75/20-25 Nm/lb.in. 4.5-5.5/40-48 Nm/lb.in.	
Weight	Typical including screws		125	g

Data according to IEC 60747 and refer to a single thyristor/diode unless otherwise stated.

Features

- International standard package
- Direct copper bonded Al₂O₃-ceramic base plate
- Planar passivated chips
- Isolation voltage 3600 V~
- UL registered, E 72873
- Keyed gate/cathode twin pins

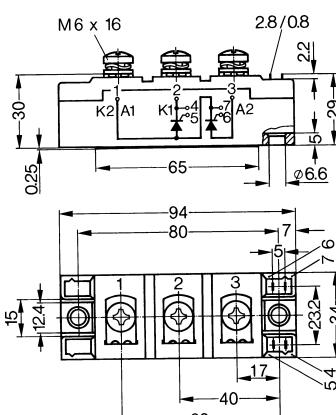
Applications

- Motor control
- Power converter
- Heat and temperature control for industrial furnaces and chemical processes
- Lighting control
- Contactless switches

Advantages

- Space and weight savings
- Simple mounting
- Improved temperature and power cycling
- Reduced protection circuits

Dimensions in mm (1 mm = 0.0394")



Symbol	Conditions	Characteristic Values		
I_{RRM}, I_{DRM}	$T_{VJ} = T_{VJM}; V_R = V_{RRM}; V_D = V_{DRM}$	10	mA	
V_T, V_F	$I_T, I_F = 120 \text{ A}; T_{VJ} = 25^\circ\text{C}$	1.13	V	
V_{TO}	$T_{VJ} = 125^\circ\text{C}$; For power-loss calculations only	0.85	V	
r_T	$T_{VJ} = T_{VJM}$	2	$\text{m}\Omega$	
V_{GT}	$V_D = 6 \text{ V}; T_{VJ} = 25^\circ\text{C}$ $T_{VJ} = -40^\circ\text{C}$	1.4 1.6	V	
I_{GT}	$V_D = 6 \text{ V}; T_{VJ} = 25^\circ\text{C}$ $T_{VJ} = -40^\circ\text{C}$	150 200	mA	
V_{GD}	$T_{VJ} = T_{VJM}; V_D = \frac{2}{3} V_{DRM}$	0.2	V	
I_{GD}		10	mA	
I_L	$T_{VJ} = 25^\circ\text{C}; t_p = 10 \mu\text{s}, V_D = 6 \text{ V}$ $I_G = 0.45 \text{ A}; di_G/dt = 0.45 \text{ A}/\mu\text{s}$	300	mA	
I_H	$T_{VJ} = 25^\circ\text{C}; V_D = 6 \text{ V}; R_{GK} = \infty$	200	mA	
t_{gd}	$T_{VJ} = 25^\circ\text{C}; V_D = \frac{1}{2} V_{DRM}$ $I_G = 0.45 \text{ A}; di_G/dt = 0.45 \text{ A}/\mu\text{s}$	2	μs	
t_q	$T_{VJ} = T_{VJM}; I_T = 120 \text{ A}, t_p = 200 \mu\text{s}; -di/dt = 10 \text{ A}/\mu\text{s typ.}$ $V_R = 100 \text{ V}; dv/dt = 20 \text{ V}/\mu\text{s}; V_D = \frac{2}{3} V_{DRM}$	150	μs	
Q_s	$T_{VJ} = T_{VJM}; I_T, I_F = 200 \text{ A}, -di/dt = 50 \text{ A}/\mu\text{s}$	330	μC	
I_{RM}		180	A	
R_{thJC}	per thyristor/diode; DC current	0.2	K/W	
	per module	0.1	K/W	
R_{thCH}	per thyristor/diode; DC current	typ.	0.1	K/W
d_s	Creepage distance on surface	12.7	mm	
d_A	Strike distance through air	9.6	mm	
a	Maximum allowable acceleration	50	m/s^2	

Optional accessories for modules

Keyed gate/cathode twin plugs with wire length = 350 mm, gate = yellow, cathode = red

Type **ZY 180L** (L = Left for pin pair 4/5) } UL Styles 1385,

Type **ZY 180R** (R = right for pin pair 6/7) } CSA Class 5851, File 41234