

1400 PROVIDENCE HIGHWAY • BUILDING 2 NORWOOD, MASSACHUSETTS 02062-5015 WWW.INTRONICS.COM

## **S10 & S15 SERIES DC/DC MODULES**

## Applications Networking Gear

- · Servers, Switches and Data Storage Wireless Communications
- - Distributed Power Architecture Semiconductor Test Equipment

 Data Communications Telecommunications

Industrial / Medical

The S10 & S15 Families of high efficiency non-isolated DC/DC converters offer power levels of up to 82 Watt, which exceeds that of other Industry-standard SMT and Through-Hole SIPs with the same package, while also providing ultra-wide input voltage range for 3.3Vin and 5Vin. These converters provide versatility without sacrificing the board space. All models feature an input filter and regulated outputs. The open-frame construction facilitates maximum power delivered with the highest efficiency of up to 95%. All converters combine creative design practices with highly derated power devices to achieve very high reliability, high performance and low cost solution to systems designers.

## **Specifications & Features Summary**

- Industry Standard SIP or SMT Pinout
- High Efficiency to 95%
- Ultra-wide 3.0 to 5.5Vin Range
- User-Adjustable 0.9 3.63V Output .
- Over Temperature Protection
- **Continuous Short Circuit Protection**
- Remote ON/OFF
- Pending UL/C UL 60950 Approvals
- PLEASE ADD SUFFIX "T" FOR THROUGH-HOLE PACKAGE

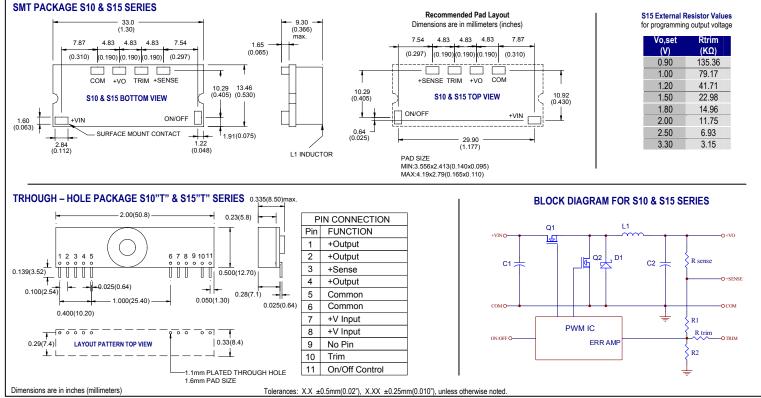
MODEL	INPUT	OUTPUT	OUTPUT	INPUT C	URRENT	%	
NUMBER	VOLTAGE	VOLTAGE	CURRENT	NO LOAD	FULL LOAD	EFF	PACKAGE
S10-5S1.0	3.0 –5.5VDC	1.0 Vdc	10 A	50 mA	2353mA	85	SIP / SMT
S10-5S1.2	3.0 -5.5VDC	1.2 Vdc	10 A	50 mA	2791mA	86	SIP / SMT
S10-5S1.5	3.0 –5.5VDC	1.5 Vdc	10 A	50 mA	3409mA	88	SIP / SMT
S10-5S1.8	3.0 -5.5VDC	1.8 Vdc	10 A	50 mA	4000mA	90	SIP / SMT
S10-5S2.0	3.0 -5.5VDC	2.0 Vdc	10 A	60 mA	4396mA	91	SIP / SMT
S10-5S2.5	3.0 -5.5VDC	2.5 Vdc	10 A	60 mA	5376mA	93	SIP / SMT
S10-5S3.3	4.5 –5.5VDC	3.3 Vdc	10 A	60 mA	6947mA	95	SIP / SMT
S15-5S3.3	3.0-5.5VDC	0.9-3.63 Vdc	15A	Various	Various	94 max.	SIP / SMT

Typical at Ta= +25 °C under nominal input voltages of 5V and 12VDC, unless noted. The information and specifications contained in this brief are believed to be accurate and reliable at the time of publication. Specifications are subject to change without notice. Refer to product specification sheet for performance characteristics and application guidelines

Consult factory for hundreds of other available input/output voltage configurations.

INPUT SPECIF	ICATIONS						
Under Voltage Lock-out Power up	2.8V typ.						
Power down	2.7V typ.						
Input Filter Type	Capacitive						
Remote on/off Control :							
Module ON	Open Circuit or <0.4VDC						
Module OFF	>+2.8VDC to Vin						
OUTPUT SPECIFICATIONS							
Voltage Accuracy	±1.5% max.						
Transient Response: 25% Step Load Change	<200u sec.						
Ripple and Noise, 20MHz BW							
Note 3, 20mV rms max.	50mV pk-pk max.						
Temperature Coefficient	±0.03%/C max.						
Short Circuit Protection	Continuous						
Line Regulation, Note1	±0.2% max., (SMT S15 is ±0.4% max.						
Load Regulation, Note2	±0.5% max						
External Trim Adj. Range (S10 Family)	±10%						
Efficiency	See Table						
Isolation Voltage	Not Isolated						
Switching Frequency	300KHz typ						
Over Temperature Protection	120°C typ						
Operating Ambient Temperature Range	-40°C to +85°C						
Derating Temperature	See Application Notes						
Storage Temperature Range	-55°C to +125°C						
Dimensions:							
(Through-Hole Package)	2"x0.5"x0.33" (50.8x12.7x8.3 mm)						
(SMT Package)	1.3"x0.53"x0.366" (33.00x13.46x9.3 mm)						
Structure	Non-potted With Open Frame Type						
NOTES							
1. Measured from High Line to Low Line	S15 Family Vo, set =1.8Vdc						
2. Measured from Full Load to Zero Load	S15 Family Vo, set =3.3Vdo						
3 Moscured with 10uf tantalum can							

1. Measured from High Line to Low Line	S15 Family Vo, set =1.8Vdc
2. Measured from Full Load to Zero Load	S15 Family Vo, set =3.3Vdc
3. Measured with 10uf tantalum cap	
& 1uf ceramic cap across output.	
4. 100uf, ESR <20mΩ Cap across Vin is	
recommended to reduce ripple	



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