

CD54AC573/3A CD54ACT573/3A

Octal Transparent Latch Three-State, Non-Inverting

June 1997

COMPLETE DATA SHEET COMING SOON!

Description

The CD54AC573/3A and CD54ACT573/3A are octal transparent three-state latches that utilize the Harris Advanced CMOS Logic technology. The outputs are transparent to the inputs when the Latch Enable ($\overline{\text{LE}}$) is HIGH. When the Latch Enable ($\overline{\text{DE}}$) goes LOW, the data is latched. The Output Enable ($\overline{\text{OE}}$) controls the three-state outputs. When the Output Enable ($\overline{\text{OE}}$) is HIGH, the outputs are in the high-impedance state. The latch operation is independent of the state of the Output Enable.

The CD54AC573/3A and CD54ACT573/3A are supplied in 20 lead dual-in-line ceramic packages (F suffix).

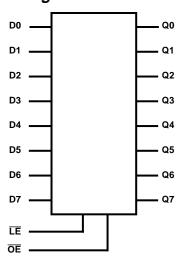
ACT INPUT LOAD TABLE

INPUT	UNIT LOAD (NOTE 1)
ŌĒ	0.87
Dn	0.5
ΙĒ	0.8

NOTE:

1. Unit load is ΔI_{CC} limit specified in DC Electrical Specifications Table, e.g., 2.4mA Max at +25°C.

Functional Diagram



Absolute Maximum Ratings

DC Supply Voltage, V _{CC} 0.5V to +6V
DC Input Diode Current, I _{IK}
For $V_1 < -0.5V$ or $V_1 > V_{CC} + 0.5V$ ±20mA
DC Output Diode Current, I _{OK}
For $V_O < -0.5V$ or $V_O > V_{CC} + 0.5V$
DC Output Source or Sink Current, Per Output Pin, IO
For $V_O > -0.5V$ or $V_O < V_{CC} + 0.5V$
DC V _{CC} or GND Current, I _{CC} or I _{GND}
For Up to 4 Outputs Per Device, Add ±25mA For Each
Additional Output

Power Dissipation Per Package, P_D $T_A = -55^{\circ}C$ to +100°C (Package F)
Operating Temperature Range, T _A Package Type F55°C to +125°C Storage Temperature, T _{STG} 65°C to +150°C
Lead Temperature (During Soldering) At Distance 1/16in. ± 1/32in. (1.59mm ± 0.79mm)
From Case For 10s Max

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

Recommended Operating Conditions

Supply Voltage Range, V _{CC}
Unless Otherwise Specified, All Voltages Referenced to GND
T _A = Full Package Temperature Range
CD54AC Types
CD54ACT Types
DC Input or Output Voltage, $V_I, V_O.$ 0V to V_{CC}

Operating Temperature, T _A	55°C to +125°C
at 1.5V to 3V (AC Types)	0ns/V to 50ns/V
at 3.6V to 5.5V (AC Types)	0ns/V to 20ns/V
at 4.5V to 5.5V (AC Types)	0ns/V to 10ns/V