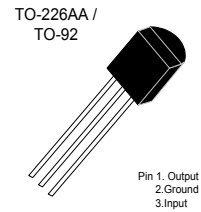


THREE-TERMINAL LOW CURRENT POSITIVE VOLTAGE REGULATORS

The IL79LXX, A Series negative voltage regulators are inexpensive, easy-to-use devices suitable for numerous applications requiring up to 100 mA. This series features thermal shutdown and current limiting, making them remarkably rugged. In most applications, no external components are required for operation.

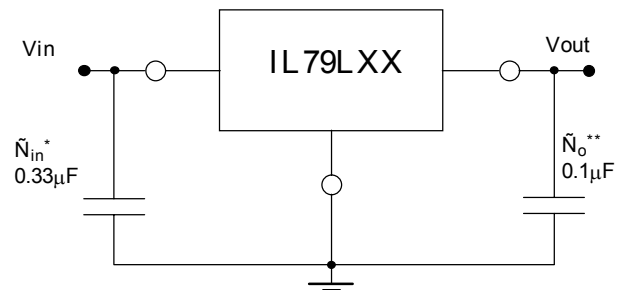
The IL79LXX devices are useful for on-card regulation or any other application where a regulated negative voltage at a modest current level is needed. These regulators offer substantial advantage over the common resistor/zener diode approach.



FEATURES

- No External Components Required
- Internal Short Circuit Current Limiting
- Internal Thermal Overload Protection
- Low Cost
- Complementary Positive Regulators Offered (IL78LXX Series)
- Available in Either $\pm 5\%$ (AC) or $\pm 10\%$ (C) Selections

Standard application



A common ground is required between the input and the output voltages. The input voltage must remain typically 2.0 V above the output voltage even during the low point on the input ripple voltage.

*C in is required if regulator is located an appreciable distance from power supply filter.

**C O is not needed for stability; however, it does improve transient response.

ABSOLUTE MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Input Voltage (-5.0 V) (-12, -15, -18V) (-24V)	V_i	-30 -35 -40	Vdc
Storage Temperature Range	Tstg	-65 to +150	°C
Operating Junction Temperature Range	T _J	+150	°C

IL79LXX

IL79L05 ELECTRICAL CHARACTERISTICS

($V_i = -10\text{ V}$, $I_o = 40\text{ mA}$, $C_i = 0.33\text{ }\mu\text{F}$, $C_o = 0.1\text{ }\mu\text{F}$, $-40^\circ\text{C} < T_J < +125^\circ\text{C}$ unless otherwise noted.)

Characteristics	Symbol	Min	Typ	Max	Unit
Output Voltage ($T_J = +25^\circ\text{C}$)	V_o	-4.8	-5.0	-5.2	Vdc
Line Regulation ($T_J = +25^\circ\text{C}$) -7.0 Vdc $\geq V_i \geq 20\text{ Vdc}$ -8.0 Vdc $\geq V_i \geq 20\text{ Vdc}$	Reg_{line}	- -	- -	150 100	mV
Load Regulation $T_J = +25^\circ\text{C}$, $1.0\text{ mA} \leq I_o \leq 100\text{ mA}$ $1.0\text{ mA} \leq I_o \leq 40\text{ mA}$)	Peg_{load}	- -	- -	60 30	mV
Output Voltage -7.0 Vdc $\geq V_i \geq -20\text{ Vdc}$, ($V_i = -10\text{ V}$, $1.0\text{ mA} \leq I_o \leq 70\text{ mA}$)	V_o	-4.75 -4.75	- -	-5.25 -5.25	Vdc
Input Bias Current ($T_J = +25^\circ\text{C}$) ($T_J = +125^\circ\text{C}$)	I_{IB}	- -	- -	6.0 5.5	mA
Input Bias Current Change -8.8 Vdc $\geq V_i \geq -20\text{ Vdc}$ $1.0\text{ mA} \leq I_o \leq 40\text{ mA}$	ΔI_{IB}	- -	- -	1.5 0.1	mA
Output Noise Voltage ($T_A = +25^\circ\text{C}$, $10\text{ Hz} \leq f \leq 100\text{ kHz}$)	V_n	-	40	-	μV
Ripple Rejection (-8.0 Vdc $\geq V_i \geq -18\text{ V}$, $f = 120\text{ Hz}$, $T_J = +25^\circ\text{C}$)	RR	41	49	-	dB
Dropout Voltage ($I_o = 40\text{ mA}$, $T_J = +25^\circ\text{C}$)	$V_i - V_o$	-	1.7	-	Vdc

IL79L12 ELECTRICAL CHARACTERISTICS

($V_i = -19\text{ V}$, $I_o = 40\text{ mA}$, $C_i = 0.33\text{ }\mu\text{F}$, $C_o = 0.1\text{ }\mu\text{F}$, $-40^\circ\text{C} < T_J < +125^\circ\text{C}$, unless otherwise noted.)

Characteristics	Symbol	Min	Typ	Max	Unit
Output Voltage ($T_J = +25^\circ\text{C}$)	V_o	-11.1	-12	-12.9	Vdc
Line Regulation ($T_J = +25^\circ\text{C}$) -14.5Vdc $\geq V_i \geq 27\text{ Vdc}$ -16 Vdc $\geq V_i \geq 27\text{ Vdc}$	Reg_{line}	- -	- -	250 200	mV
Load Regulation $T_J = +25^\circ\text{C}$, $1.0\text{ mA} \leq I_o \leq 100\text{ mA}$ $1.0\text{ mA} \leq I_o \leq 40\text{ mA}$	Peg_{load}	- -	- -	100 50	mV
Output Voltage -14.5Vdc $\geq V_i \geq -27\text{ Vdc}$, $1.0\text{ mA} \leq I_o \leq 40\text{ mA}$ $V_i = -19\text{ V}$, $1.0\text{ mA} \leq I_o \leq 70\text{ mA}$)	V_o	-10.8 -- 10.8	- -	-13.2 -13.2	Vdc
Input Bias Current ($T_J = +25^\circ\text{C}$) ($T_J = +125^\circ\text{C}$)	I_{IB}	- -	- -	6.5 6.0	mA
Input Bias Current Change -16Vdc $\geq V_i \geq -27\text{ Vdc}$ $1.0\text{ mA} \leq I_o \leq 40\text{ mA}$	ΔI_{IB}	- -	- -	1.5 0.2	mA
Output Noise Voltage ($T_A = +25^\circ\text{C}$, $10\text{ Hz} \leq f \leq 100\text{ kHz}$)	V_n	-	80	-	μV
Ripple Rejection -15V $\geq V_i \geq -25\text{ V}$, $f = 120\text{ Hz}$, $T_J = +25^\circ\text{C}$)	RR	36	42	-	dB
Dropout Voltage ($I_o = 40\text{ mA}$, $T_J = +25^\circ\text{C}$)	$V_i - V_o$	"	1.7	-	Vdc

IL79LXX

IL79L15 ELECTRICAL CHARACTERISTICS

($V_i = -23\text{ V}$, $I_o = 40\text{ mA}$, $C_i = 0.33\text{ }\mu\text{F}$, $C_o = 0.1\text{ }\mu\text{F}$, $-40^\circ\text{C} < T_J < +125^\circ\text{C}$), unless otherwise noted.)

Characteristics	Symbol	Min	Typ	Max	Unit
Output Voltage ($T_J = +25^\circ\text{C}$)	V_o	-13.8	-15	-16.2	Vdc
Line Regulation ($T_J = +25^\circ\text{C}$, $I_o = 40\text{ mA}$) $-30\text{ Vdc} \leq V_i \leq -17.5\text{ Vdc}$ $-30\text{ Vdc} \leq V_i \leq -20\text{ Vdc}$	Reg_{line}	-	-	300 250	mV
Load Regulation ($T_J = +25^\circ\text{C}$, $1.0\text{ mA} \leq I_o \leq 100\text{ mA}$) ($T_J = +25^\circ\text{C}$, $1.0\text{ mA} \leq I_o < 40\text{ mA}$)	Peg_{load}	-	-	150 75	mV
Output Voltage ($17.5\text{ Vdc} \leq V_i \leq 30\text{ Vdc}$, $1.0\text{ mA} \leq I_o \leq 40\text{ mA}$) ($V_i = 23\text{ V}$, $1.0\text{ mA} \leq I_o \leq 70\text{ mA}$)	V_o	-13.5 -13.5	-	-16.5 -16.5	Vdc
Input Bias Current ($T_J = +25^\circ\text{C}$) ($T_J = +125^\circ\text{C}$)	I_{IB}	-	-	6.5 6.0	mA
Input Bias Current Change ($20\text{ Vdc} \leq V_i \leq 30\text{ Vdc}$) ($1.0\text{ mA} \leq I_o \leq 40\text{ mA}$)	ΔI_{IB}	-	-	1.5 0.2	mA
Output Noise Voltage ($T_A = +25^\circ\text{C}$, $10\text{ Hz} \leq f \leq 100\text{ kHz}$)	V_n	-	90	-	nV
Ripple Rejection ($I_o = 40\text{ mA}$, $f = 120\text{ Hz}$, $18.5\text{ V} \leq V_i \leq 28.5\text{ V}$, $T_J = +25^\circ\text{C}$)	RR	33	39	-	dB
Dropout Voltage ($T_J = +25^\circ\text{C}$)	$ V_i - V_o $	-	1.7	-	Vdc

IL79L18 ELECTRICAL CHARACTERISTICS

($V_i = 27\text{ V}$, $I_o = 40\text{ mA}$, $C_i = 0.33\text{ }\mu\text{F}$, $C_o = 0.1\text{ }\mu\text{F}$, $40^\circ\text{C} < T_J < +125^\circ\text{C}$), unless otherwise noted.)

Characteristics	Symbol	Min	Typ	Max	Unit
Output Voltage ($T_J = +25^\circ\text{C}$)	V_o	-16.6	-18	-19.4	Vdc
Line Regulation ($T_J = +25^\circ\text{C}$, $I_o = 40\text{ mA}$) $-33\text{ Vdc} \leq V_i \leq -20.7\text{ Vdc}$ $-33\text{ Vdc} \leq V_i \leq -22\text{ Vdc}$	Reg_{line}	-	-	325 275	mV
Load Regulation ($T_J = +25^\circ\text{C}$, $1.0\text{ mA} \leq I_o \leq 100\text{ mA}$) ($T_J = +25^\circ\text{C}$, $1.0\text{ mA} \leq I_o \leq 40\text{ mA}$)	Peg_{load}	-	-	170 85	mV
Output Voltage ($-33\text{ Vdc} \leq V_i \leq -21.4\text{ Vdc}$, $1.0\text{ mA} \leq I_o \leq 40\text{ mA}$) ($V_i = -27\text{ V}$, $1.0\text{ mA} \leq I_o \leq 70\text{ mA}$)	V_o	-16.2 -16.2	-	-19.8 -19.8	Vdc
Input Bias Current ($T_J = +25^\circ\text{C}$) ($T_J = +125^\circ\text{C}$)	I_{IB}	-	-	6.5 6.0	mA
Input Bias Current Change ($-33\text{ Vdc} \leq V_i \leq -22\text{ Vdc}$) ($1.0\text{ mA} \leq I_o \leq 40\text{ mA}$)	ΔI_{IB}	-	-	1.5 0.2	mA
Output Noise Voltage ($T_A = +25^\circ\text{C}$, $10\text{ Hz} \leq I_o \leq 100\text{ kHz}$)	V_n	-	150	-	nV
Ripple Rejection ($f = 120\text{ Hz}$, $-33\text{ V} \leq V_i \leq -23\text{ V}$, $T_J = +25^\circ\text{C}$)	RR	32	46	-	dB
Dropout Voltage ($T_J = +25^\circ\text{C}$)	$ V_i - V_o $	-	1.7	-	Vdc

IL79L24 ELECTRICAL CHARACTERISTICS

($V_i = 33\text{ V}$, $I_o = 40\text{ mA}$, $C_i = 0.33\text{ }\mu\text{F}$, $C_o = 0.1\text{ }\mu\text{F}$,
 $0^\circ\text{C} < T_J < +125^\circ\text{C}$, unless otherwise noted.)

Characteristics	Symbol	Min	Typ	Max	Unit
Output Voltage ($T_J = +25^\circ\text{C}$)	V_o	-22.1	-24	-25.9	Vdc
Line Regulation ($T_J = +25^\circ\text{C}$, $I_o = 40\text{ mA}$) $38\text{ Vdc} \leq V_i \leq 27.5\text{ Vdc}$ $38\text{ Vdc} \leq V_i \leq 28\text{ Vdc}$	Reg_{line}	- -	35 30	350 300	mV
Load Regulation ($T_J = +25^\circ\text{C}$, $1.0\text{ mA} \leq I_o \leq 100\text{ mA}$) ($T_J = +25^\circ\text{C}$, $1.0\text{ mA} \leq I_o \leq 40\text{ mA}$)	Reg_{load}	- -	40 20	200 100	mV
Output Voltage $-38\text{ Vdc} \leq V_i \leq -28\text{ Vdc}$, $1.0\text{ mA} \leq I_o \leq 40\text{ mA}$ $V_i = -33\text{ Vdc}$, $1.0\text{ mA} \leq I_o \leq 70\text{ mA}$	V_o	-21.6 -21.6	- -	-26.4 -26.4	Vdc
Input Bias Current ($T_J = +25^\circ\text{C}$) ($T_J = +125^\circ\text{C}$)	I_{IB}	- -	- -	6.5 6.0	mA
Input Bias Current Change ($-38\text{ Vdc} \leq V_i \leq -28\text{ Vdc}$) ($1.0\text{ mA} \leq I_o \leq 40\text{ mA}$)	ΔI_{IB}	- -	- -	1.5 0.2	mA
Output Noise Voltage ($T_A = +25^\circ\text{C}$, $10\text{ Hz} \leq f \leq 100\text{ kHz}$)	V_n	-	200	-	nV
Ripple Rejection ($I_o = 40\text{ mA}$, $f = 120\text{ Hz}$, $-35\text{ V} \leq V_i \leq -29\text{ V}$, $T_J = +25^\circ\text{C}$)	RR	30	43	-	dB
Dropout Voltage ($T_J = +25^\circ\text{C}$)	$ V_i - V_o $	-	1.7	-	Vdc