QUICKSWITCH® PRODUCTS HIGH-SPEED CMOS 10-BIT BUS SWITCH WITH FLOW-THROUGH PINOUT

IDTQS32861

FEATURES:

- Enhanced N channel FET with no inherent diode to Vcc
- Near zero ground bounce
- Undershoot clamp diodes on all switch and control inputs
- Available in QSOP and SOIC packages

APPLICATIONS:

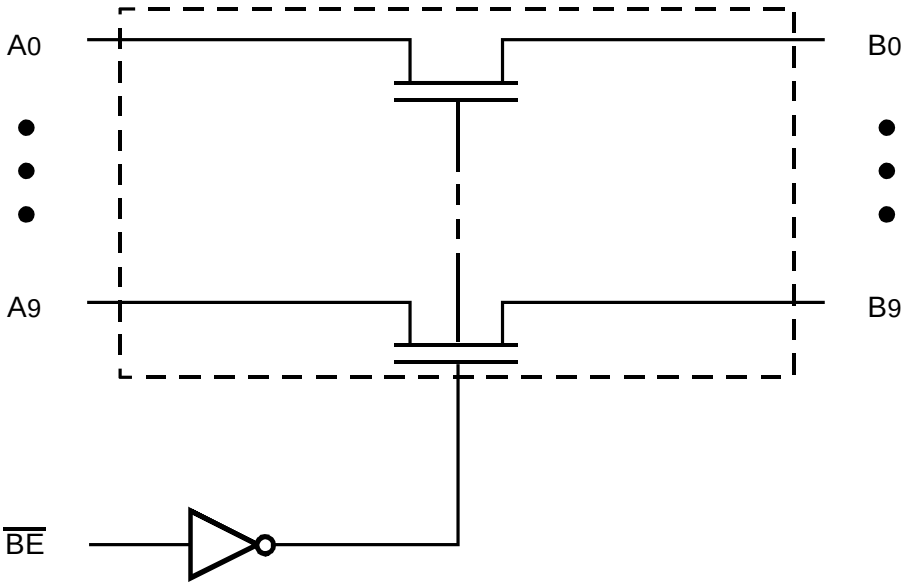
- Hot-swapping, hot-docking
- Voltage translation (5V to 3.3V)
- Power conservation
- Capacitance reduction and isolation
- Clock gating
- Bus isolation

DESCRIPTION:

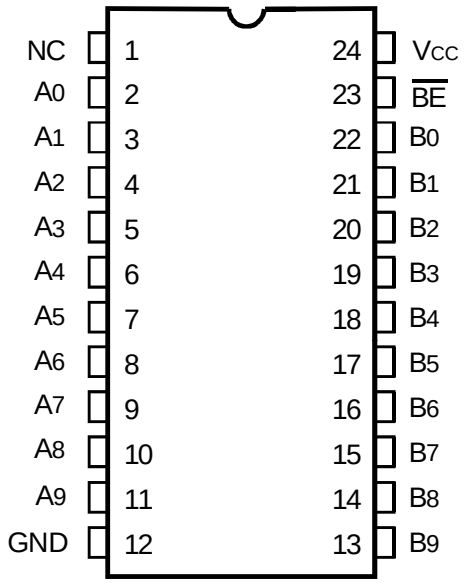
The QS32861 provides a set of ten high speed CMOS, TTL-compatible bus switches. The Bus Enable ($\overline{BE}$) signal turns the switches on. The QS32861 includes internal 25Ω series termination resistors to reduce reflection noise in high speed applications. When closed, the switch acts as the source (series) termination for the driver connected to it.

The QS32861 is characterized for operation at -40°C to +85°C.

FUNCTIONAL BLOCK DIAGRAM



PIN CONFIGURATION



QSOP/ SOIC
TOP VIEW

ABSOLUTE MAXIMUM RATINGS(1)

Symbol	Description	Max	Unit
V_{TERM}(2)	Supply Voltage to Ground	–0.5 to +7	V
V_{TERM}(3)	DC Switch Voltage Vs	–0.5 to +7	V
V_{TERM}(3)	DC Input Voltage V_{IN}	–0.5 to +7	V
V_{AC}	AC Input Voltage (pulse width ≤ 20ns)	–3	V
I_{OUT}	DC Output Current	120	mA
P_{MAX}	Maximum Power Dissipation (T_A = 85°C)	0.5	W
T_{STG}	Storage Temperature	–65 to +150	°C

NOTE:
1. Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.
2. Vcc terminals.
3. All terminals except Vcc.

CAPACITANCE

(T_A = +25°C, f = 1.0MHz, V_{IN} = 0V, V_{OUT} = 0V)

Pins	Typ.	Max.(1)	Unit
Control Pins	3	5	pF
Quickswitch Channels (Switch OFF)	5	7	pF

NOTE:
1. This parameter is measured at characterization but not tested.

PIN DESCRIPTION

Pin Names	Description
$\overline{BE}$	Bus Enable
A_0 - A_9	Bus A
B_0 - B_9	Bus B

FUNCTION TABLE(1)

$\overline{BE}$	A_0 - A_9	2A, 2Y I/Os
H	Hi-Z	Disconnect
L	B_0 - B_9	Connect

NOTE:
1. H = HIGH Voltage Level
   L = LOW Voltage Level
   Z = High-Impedance

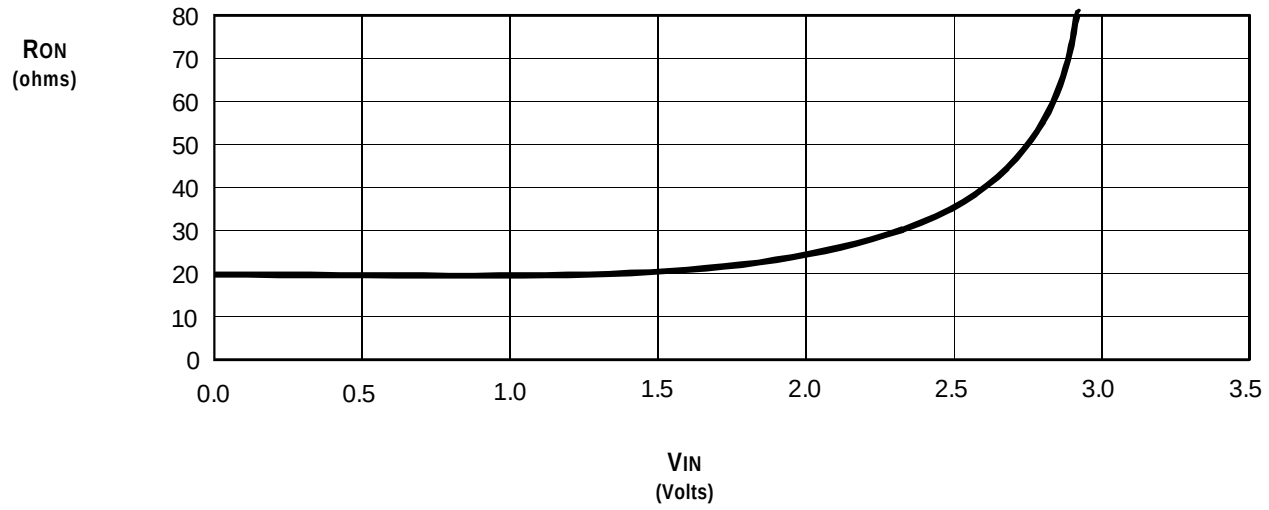
DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

Following Conditions Apply Unless Otherwise Specified:
Industrial: T_A = –40°C to +85°C, V_{CC} = 5.0V ±5%

Symbol	Parameter	Test Conditions	Min.	Typ.[1]	Max.	Unit
V_{IH}	Input HIGH Level	Guaranteed Logic HIGH for Control Pins	2	—	—	V
V_{IL}	Input LOW Level	Guaranteed Logic LOW for Control Pins	—	—	0.8	V
I_{IN}	Input LeakageCurrent (Control Inputs)[2]	0V ≤ V_{IN} ≤ V_{CC}	—	±0.01	±1	µA
I_{OZ}	Off-State Output Current (Hi-Z)	0V ≤ V_{OUT} ≤ V_{CC}, Switches OFF	—	±0.01	±1	µA
R_{ON}[3]	Switch ON Resistance	V_{CC} = Min., V_{IN} = 0V, I_{ON} = 30mA	15	20	35	Ω
		V_{CC} = Min., V_{IN} = 2.4V, I_{ON} =15mA	18	25	40	
V_P	Pass Voltage[2]	V_{IN} = V_{CC} = 5V, I_{OUT} = -5µA	3.7	4	4.2	V

NOTES:
1. Typical values are at V_{CC} = 5.0V, T_A = 25°C.
2. Pass Voltage is guaranteed but not production tested.
3. R_{OUT} changed on March 8, 2002. See rear page for more information.

TYPICAL ON RESISTANCE vs V_{IN} AT V_{CC} = 5V



POWER SUPPLY CHARACTERISTICS

Symbol	Parameter	Test Conditions[1]	Typ[2]	Max.	Unit
I_{CCQ}	Quiescent Power Supply Current	V_{CC} = Max., V_{IN} = GND or Vcc, f = 0	0.2	3	µA
ΔI_{CC}	Power Supply Current per Control Input HIGH	V_{CC} = Max., V_{IN} = 3.4V[3], f = 0	—	2.5	mA
I_{CCD}	Dynamic Power Supply Current per MHz[4]	V_{CC} = Max., A and B pins open $\overline{BE}$ Input Toggling at 50% Duty Cycle	—	0.25	mA/MHz

NOTES:
1. For conditions shown as Min. or Max., use the appropriate values specified under DC Electrical Characteristics.
2. Typical values are at V_{CC} = 5.0V, 25°C ambient.
3. Per TLL driven input (V_{IN} = 3.4V, control inputs only). A and B pins do not contribute to ΔI_{CC}.
4. This current applies to the control inputs only and represents the current required to switch internal capacitance at the specified frequency. The A and B pins generate no significant AC or DC currents as they transition. This parameter is guaranteed but not production tested.

SWITCHING CHARACTERISTICS OVER OPERATING RANGE

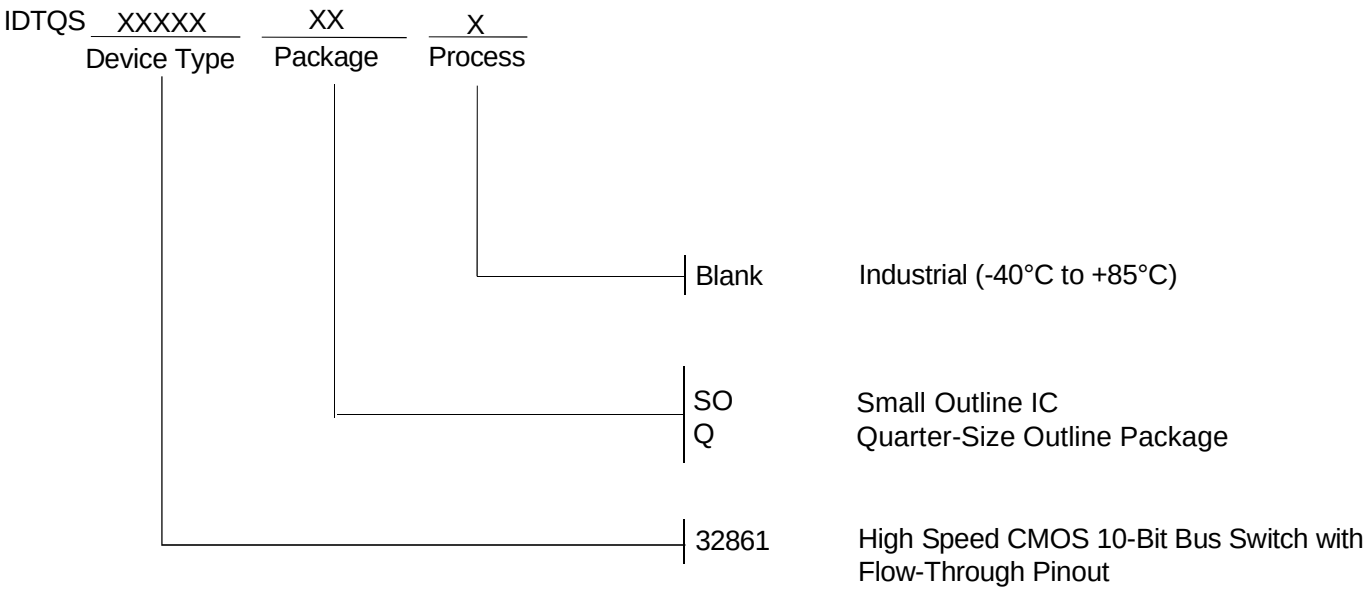
T_A = -40°C to +85°C, V_{CC} = 5.0V ± 5%;

C_{LOAD} = 50pF, R_{LOAD} = 500Ω unless otherwise noted.

Symbol	Parameter	Min.[1]	Typ.	Max.	Unit
t_{PLH} t_{PHL}	Data Propagation Delay[2,4] A to B or B to A	—	—	1.25[3]	ns
t_{PZH} t_{PZL}	Switch Turn-on Delay $\overline{BE}$ to A or B	1.5	—	7.5	ns
t_{PHZ} t_{PLZ}	Switch Turn-off Delay[2] $\overline{BE}$ to A or B	1.5	—	5.5	ns

NOTES:
1. Minimums are guaranteed but not production tested.
2. This parameter is guaranteed but not production tested.
3. The time constant for the switch alone is of the order of 1.25ns for C_L = 50pF.
4. The bus switch contributes no propagation delay other than the RC delay of the ON resistance of the switch and the load capacitance. Since this time constant is much smaller than the rise and fall times of typical driving signals, it adds very little propagation delay to the system. Propagation delay of the bus switch, when used in a system, is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

ORDERING INFORMATION



As per PCN L0201-02, the Output Resistance (R_{ON}) specifications have changed as of March 8, 2002. The original specifications were:

Parameter	Description	Min.	Typ.	Max.	Unit
R_{ON}	V_{CC} = Min, V_{IN} = 0V, I_{ON} = 30mA	20	28	40	Ω
	V_{CC} = Min, V_{IN} = 2.4V, I_{ON} = 15mA	20	35	48	

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