Octal Bus Transceivers (with 3-state outputs)

# **HITACHI**

### **Description**

Both the HD74HCT640 and the HD74HCT643 have one active low enable input  $(\overline{G})$ , and a direction control (DIR). When the DIR input is high, data flows from the A inputs to the B outputs. When DIR is low, data flows from B to A.

The HD74HCT640 transfers inverted data from one bus to the other. The HD74HCT643 transfers inverted data from the A bus to the B bus and non-inverted data from the B bus to the A bus.

#### **Features**

- LSTTL Output Logic Level Compatibility as well as CMOS Output Compatibility
- High Speed Operation:  $t_{pd}$  (A to B) = 14.5 ns typ ( $C_L = 50 \text{ pF}$ )
- High Output Current: Fanout of 15 LSTTL Loads
- Wide Operating Voltage:  $V_{CC} = 4.5$  to 5.5 V
- Low Input Current: 1 μA max
- Low Quiescent Supply Current:  $I_{CC}$  (static) = 4  $\mu$ A max (Ta = 25°C)

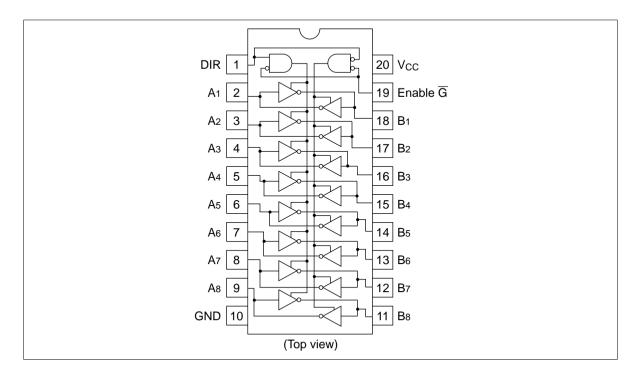
#### **Function Table**

Control Input		Operation		
G	DIR	HD74HCT640	HD74HCT643	
L	L	B data to A bus	B data to A bus	
L	Н	Ā data to B bus	A data to B bus	
Н	Х	Isolation	Isolation	

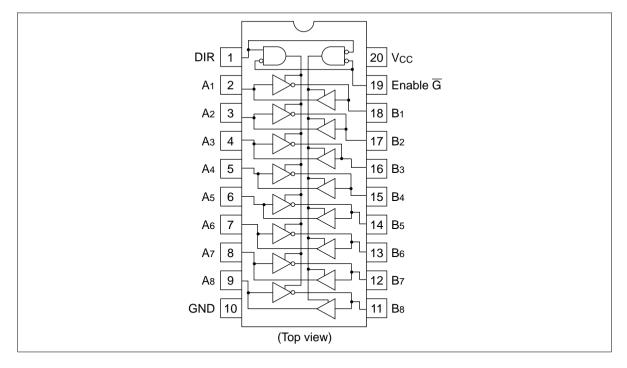


### **Pin Arrangement**

### **HD74HCT640**

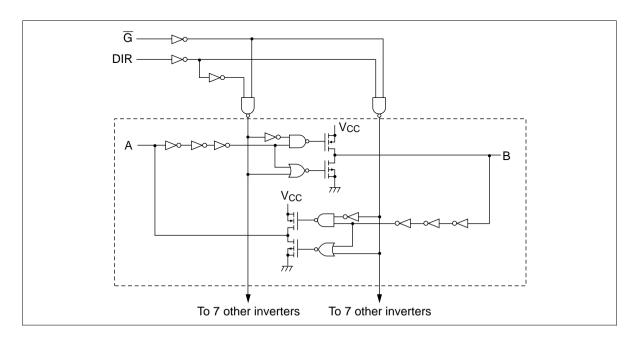


#### **HD74HCT643**



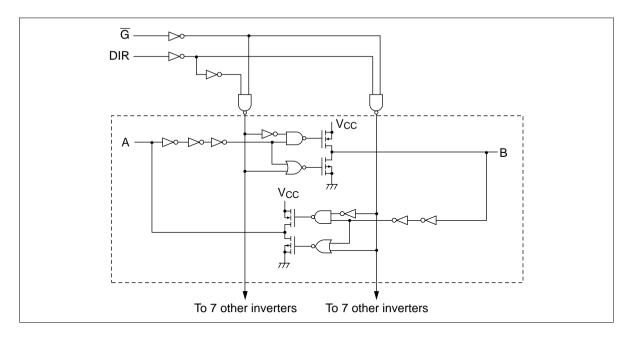
## **Block Diagram**

#### **HD74HCT640**



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### **HD74HCT643**



## **Absolute Maximum Ratings**

Item	Symbol	Rating	Unit
Supply voltage range	V <sub>cc</sub>	-0.5 to +7.0	V
Input voltage	V <sub>IN</sub>	$-0.5$ to $V_{cc} + 0.5$	V
Output voltage	$V_{\text{OUT}}$	$-0.5$ to $V_{cc}$ + 0.5	V
DC current drain per pin	I <sub>OUT</sub>	±35	mA
DC current drain per V <sub>cc</sub> , GND	I <sub>CC</sub> , I <sub>GND</sub>	±75	mA
DC input diode current	I <sub>IK</sub>	±20	mA
DC output diode current	I <sub>OK</sub>	±20	mA
Power dissipation per package	P <sub>T</sub>	500	mW
Storage temperature	Tstg	-65 to +150	°C

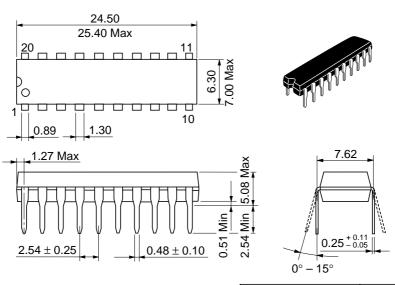
## **DC** Characteristics

		Ta =	: 25°C	;	Ta = - +85°0	–40 to		Test Co	onditions
Item	Symbol	Min	Тур	Max	Min	Max	Unit	V <sub>cc</sub> (V)	_
Input voltage	$V_{IH}$	2.0	_	_	2.0	_	V	4.5 to 5.5	
	$V_{IL}$	_	_	8.0	_	8.0	V	4.5 to 5.5	
Output voltage	$V_{OH}$	4.4	_	_	4.4	_	V	4.5	Vin = $V_{IH}$ or $V_{IL}$ $I_{OH}$ = $-20 \mu A$
		4.18	_	_	4.13	_	_	4.5	$I_{OH} = -6 \text{ mA}$
	$V_{OL}$	_	_	0.1	_	0.1	V	4.5	Vin = $V_{IH}$ or $V_{IL}$ $I_{OL}$ = 20 $\mu$ A
		_	_	0.26	_	0.33		4.5	$I_{OL} = 6 \text{ mA}$
Off-state output current	I <sub>oz</sub>	_	_	±0.5	_	±5.0	μΑ	5.5	$Vin = V_{IH} \text{ or } V_{IL},$ $Vout = V_{CC} \text{ or GND}$
Input current	lin	_	_	±0.1		±1.0	μΑ	5.5	Vin = V <sub>CC</sub> or GND
Quiescent current	I <sub>cc</sub>	_	_	4.0	_	40	μΑ	5.5	Vin = $V_{CC}$ or GND, lout = $0 \mu A$

## AC Characteristics ( $C_L = 50 \text{ pF}$ , Input $t_r = t_f = 6 \text{ ns}$ )

		Ta =	25°C	;	Ta = - +85°0	–40 to		Test Conditions
Item	Symbol	Min	Тур	Max	Min	Max	Unit	V <sub>cc</sub> (V)
Propagation delay	t <sub>PLH</sub>	_	13	18	_	23	ns	4.5
time	t <sub>PHL</sub>	_	16	18	_	23	_	4.5
Output enable	t <sub>zH</sub>	_	16	46	_	58	ns	4.5
time	t <sub>zL</sub>	_	16	46	_	58	=	4.5
Output disable	t <sub>HZ</sub>	_	17	43	_	54	ns	4.5
time	t <sub>LZ</sub>	_	21	43	_	54	=	4.5
Output rise/fall	t <sub>TLH</sub>	_	4	12	_	15	ns	4.5
time	$t_{THL}$							
Input capacitance	Cin	_	5	10	_	10	pF	_

Unit: mm



Hitachi Code	DP-20N
JEDEC	_
EIAJ	Conforms
Weight (reference value)	1.26 g

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# HITACHI

#### Hitachi, Ltd.

Semiconductor & Integrated Circuits.

Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan Tel: Tokyo (03) 3270-2111 Fax: (03) 3270-5109

Tel: Tokyo (03) 3270-2111 Fax: (03) 3270-5109

URL NorthAmerica : http:semiconductor.hitachi.com/

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#### For further information write to:

Hitachi Semiconductor (America) Inc. 179 East Tasman Drive, San Jose,CA 95134 Tel: <1> (408) 433-1990 Fax: <1>(408) 433-0223 Hitachi Europe GmbH Electronic components Group Dornacher Stra§e 3 D-85622 Feldkirchen, Munich Germany Tel: <49> (89) 9 9180-0

Fax: <49> (89) 9 29 30 00

Hitachi Europe Ltd.

Electronic Components Group.

Whitebrook Park Lower Cookham Road Maidenhead

Berkshire SL6 8YA, United Kingdom Tel: <44> (1628) 585000

Tel: <44> (1628) 585000 Fax: <44> (1628) 778322 Hitachi Asia Pte. Ltd. 16 Collyer Quay #20-00 Hitachi Tower Singapore 049318 Tel: 535-2100 Fax: 535-1533

Hitachi Asia Ltd.
Taipei Branch Office
3F, Hung Kuo Building. No.167,
Tun-Hwa North Road, Taipei (105)
Tel: <886> (2) 2718-3666

Fax: <886> (2) 2718-8180

Hitachi Asia (Hong Kong) Ltd.
Group III (Electronic Components)
7/F., North Tower, World Finance Centre,
Harbour City, Canton Road, Tsim Sha Tsui,
Kowloon, Hong Kong
Tel: <852> (2) 735 9218

Fax: <852> (2) 735 9218 Fax: <852> (2) 730 0281 Telex: 40815 HITEC HX

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