HD74HC138

3-to-8-line Decoder/Demultiplexer

HITACHI

Description

The HD74HC138 has 3 binary select inputs (A, B and C). If the device is enabled these inputs determine which one of the eight normally high outputs will go low. Two active low and one active high enables (G_1 , G_{2A} and G_{2B}) are provided to ease the cascading of decoders.

Features

• High Speed Operation: t_{pd} (A, B, C to Y) = 16.5 ns typ ($C_L = 50 \text{ pF}$)

• High Output Current: Fanout of 10 LSTTL Loads

• Wide Operating Voltage: $V_{CC} = 2 \text{ V to } 6 \text{ V}$

• Low Input Current: 1 μA max

• Low Quiescent Supply Current: I_{CC} (static) = 4 μ A max (Ta = 25°C)

Function Table

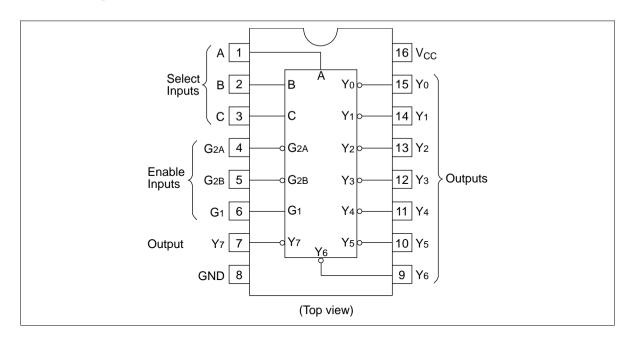
Inputs

Ena	ble		Select			Outp	Outputs									
G1	G _{2A}	G _{2B}	С	В	Α	Y ₀	Y ₁	Y ₂	Y ₃	Y ₄	Y ₅	Y ₆	Y ₇			
X	Χ	Н	Х	Χ	Χ	Н	Н	Н	Н	Н	Н	Н	Н			
Χ	Н	Χ	Х	Χ	Χ	Н	Н	Н	Н	Н	Н	Н	Н			
L	Χ	Χ	Х	Х	Х	Н	Н	Н	Н	Н	Н	Н	Н			
Н	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н			
Н	L	L	L	L	Н	Н	L	Н	Н	Н	Н	Н	Н			
Н	L	L	L	Н	L	Н	Н	L	Н	Н	Н	Н	Н			
Н	L	L	L	Н	Н	Н	Н	Н	L	Н	Н	Н	Н			
Н	L	L	Н	L	L	Н	Н	Н	Н	L	Н	Н	Н			
Н	L	L	Н	L	Н	Н	Н	Н	Н	Н	L	Н	Н			
Н	L	L	Н	Н	L	Н	Н	Н	Н	Н	Н	L	Н			
Н	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L			



HD74HC138

Pin Arrangement



DC Characteristics

			Ta =	: 25°(3	Ta = - +85°C	–40 to	_		
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Condition	าร
Input voltage	V _{IH}	2.0	1.5	_	_	1.5	_	V		
		4.5	3.15	i —	_	3.15	_	=		
		6.0	4.2	_	_	4.2	_	=		
	V _{IL}	2.0	_	_	0.5	_	0.5	V		
		4.5	_	_	1.35	_	1.35	=		
		6.0	_	_	1.8	_	1.8	=		
Output voltage	V _{OH}	2.0	1.9	2.0	_	1.9	_	V	Vin = V _{IH} or V _{IL}	$I_{OH} = -20 \mu A$
		4.5	4.4	4.5	_	4.4	_	=		
		6.0	5.9	6.0	_	5.9	_	=		
		4.5	4.18	-	_	4.13	_	=		$I_{OH} = -4 \text{ mA}$
		6.0	5.68	· —	_	5.63	_	=		$I_{OH} = -5.2 \text{ mA}$
	V _{OL}	2.0	_	0.0	0.1	_	0.1	V	$Vin = V_{IH} \text{ or } V_{IL}$	I _{OL} = 20 μA
		4.5	_	0.0	0.1	_	0.1	=		
		6.0	_	0.0	0.1	_	0.1	=		
		4.5	_	_	0.26	_	0.33	=		I _{OL} = 4 mA
		6.0	_	_	0.26	_	0.33	=		I _{OL} = 5.2 mA
Input current	lin	6.0	_	_	±0.1	_	±1.0	μΑ	Vin = V _{CC} or GN	ND
Quiescent supply current	I _{cc}	6.0	_	_	4.0	_	40	μΑ	Vin = V _{CC} or GN	ND, lout = $0 \mu A$

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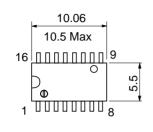
AC Characteristics ($C_L = 50 \text{ pF}$, Input $t_r = t_f = 6 \text{ ns}$)

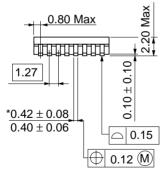
	Ta = -40 to
Ta = 25°C	+85°C

Item	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions
Propagation delay	t _{PHL}	2.0	_	_	175	_	220	ns	A, B or C to Output
time		4.5	_	17	35	_	44	=	
		6.0	_	_	30	_	37	=	
	t _{PLH}	2.0	_	_	150	_	190	ns	_
		4.5	_	16	30	_	38		
		6.0	_	_	26	_	33	=	
	t _{PHL}	2.0	_	_	150	_	190	ns	G₁ to Output
		4.5	_	16	30	_	38	=	
		6.0	_	_	26	_	33	-	
	t _{PLH}	2.0	_	_	150	_	190	ns	_
		4.5	_	17	30	_	38	=	
		6.0	_	_	26	_	33	-	
	t _{PHL}	2.0	_	_	175	_	220	ns	G _{2A} or G _{2B} to Output
		4.5	_	15	35	_	44	-	
		6.0	_	_	30	_	37	-	
	t _{PLH}	2.0	_	_	150	_	190	ns	_
		4.5	_	17	30	_	38		
		6.0	_	_	26	_	33	=	
Output rise/fall	t _{TLH}	2.0	_	_	75	_	95	ns	
time	t_{THL}	4.5	_	5	15	_	19	=	
		6.0	_	_	13	_	16	_	
Input capacitance	Cin		_	5	10		10	pF	

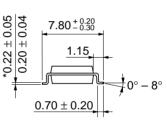
Unit: mm 19.20 20.00 Max 16 7.40 Max 6.30 1.3 1.11 Max 7.62 5.06 Max 2.54 Min 0.51 Min $0.25^{+0.13}_{-0.05}$ 0.48 ± 0.10 2.54 ± 0.25 $0^{\circ} - 15^{\circ}$ Hitachi Code DP-16 **JEDEC** Conforms EIAJ Conforms Weight (reference value) 1.07 g

Unit: mm





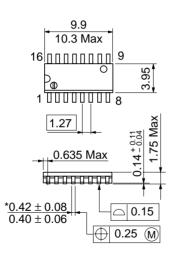


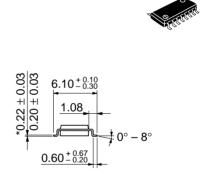


Hitachi Code	FP-16DA
JEDEC	
EIAJ	Conforms
Weight (reference value)	0.24 a

*Dimension including the plating thickness
Base material dimension

Unit: mm

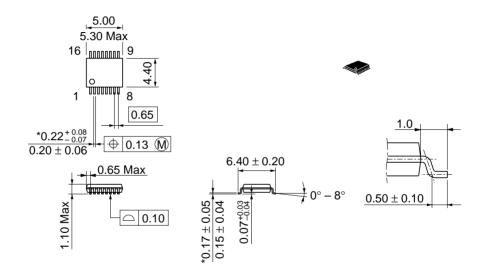




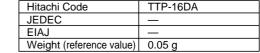
*Dimension including the plating thickness Base material dimension

Hitachi Code	FP-16DN						
JEDEC	Conforms						
EIAJ	Conforms						
Weight (reference value)	0.15 g						

Unit: mm



*Dimension including the plating thickness
Base material dimension



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