

Features and Benefits

Enhanced Feature set

- Fully DV-SD Compliant
- Automatic Audio and AUX Code Processing
- Pin compatible with NW701 (DV CODEC)

Low System Cost

- Integrated single-chip design including AV processing and video decoding
- Glue-less interface to Video Encoder (SAA7121), Audio Encoder (UDA1340), Micro-controller, and memory
- Integrated shuffle memory logic
- Requires only one 256K x 32 EDO DRAM

Real-time Performance

- High speed (33Mbytes/s throughput)
- 54MHz double clock speed for dual stream applications

Video and Audio Support

- CCIR656 Video output and I²S Audio output
- Support NTSC and PAL
- Selectable Audio channel (A/B or C/D)
- 48, 44.1, and 32KHz (12- and 16-bit) audio support

Simple Host Bus connectivity

- 8 or 16-bit asynchronous host bus interface
- built-in 512 byte DV FIFO
- Three interrupt pins for enhanced system control

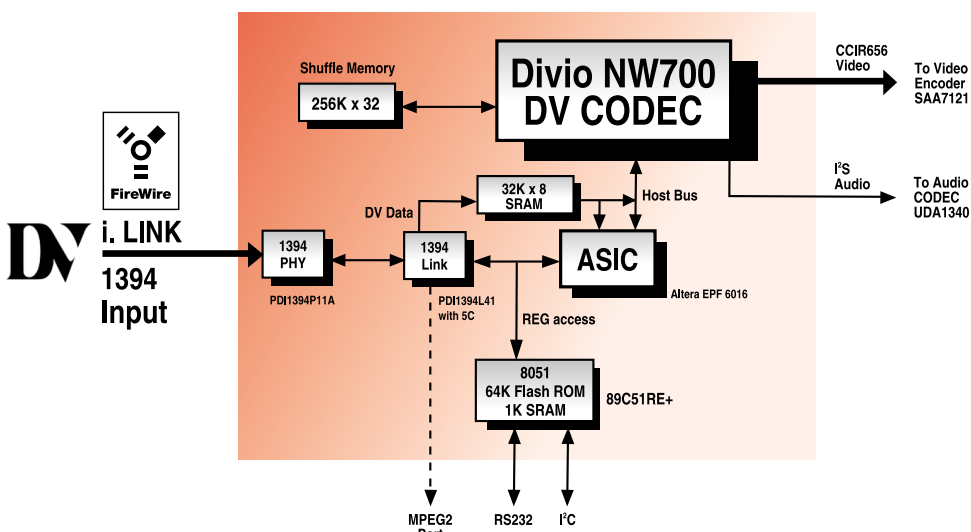
NW700 DV Decoder Daytona Beach Reference Solution

divio introduces the NW700, the world's first single-chip DV decoder. Fully DV-SD compliant and designed with divio's patented pending compression technology, the NW700 delivers unrivaled video quality, performance and compatibility. With a single-chip design and glueless interface to standard video components, divio's single chip DV decoder will replace current multi-chip solution and enable a new generation of cost-effective digital video consumer products.

divio has created the "Daytona Beach" reference solution that includes the NW700, Philips Semiconductor's PDI1394L4X audio/video 1394 LINK layer controller and PDI1394P11A 200 Mbits/sec PHY. divio provides a complete easy to integrate DV/IEEE1394 solution to OEMs to reduce time-to-market and development costs.

The NW700 provides an unprecedented feature-set that deliver real-time DV decoding functions to empower the next generation of consumer electronics devices.

DAYTONA BEACH DV Decode Module

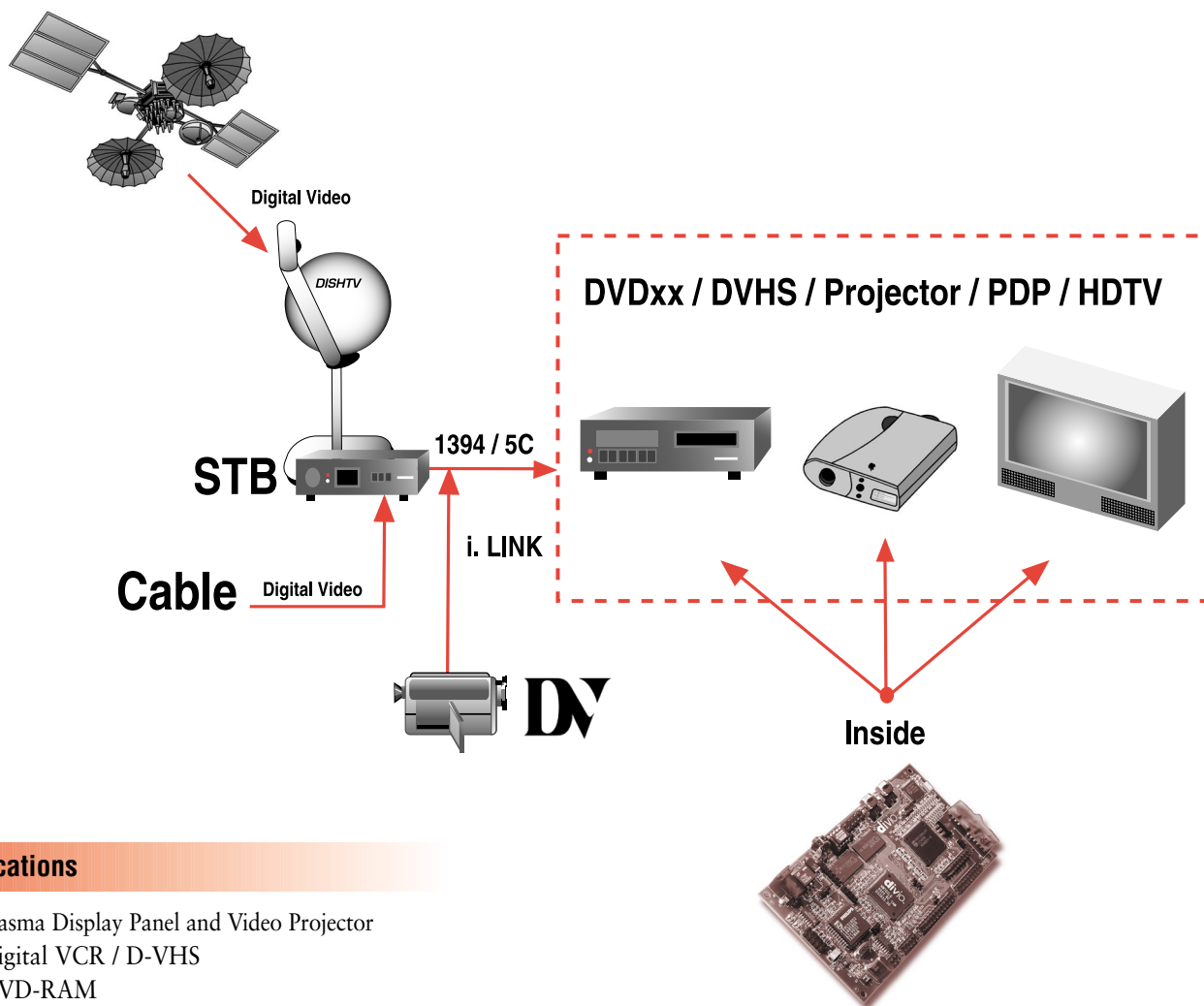


NW700

DV Decoder

Daytona Beach

Reference Solution



Applications

- Plasma Display Panel and Video Projector
- Digital VCR / D-VHS
- DVD-RAM
- Digital Set-top-box
- HDTV

Daytona Beach Reference Kit

Board Components

- divio NW700 - DV Decoder
- 8051 μ C (P89C51RD2)
- 100p FPGA/32k x 8 SRAM
- IEEE1394 LINK (PDI1394L4X)
- IEEE1394 PHY (PDI1394P11A)
- 256 x 32 EDO Memory

Manufacturing Kit Contents

- Daytona Beach Reference Design
- Schematics, Gerber Files and BOM
- Technical Documents and Manuals
- FPGA and Firmware Source Code

Ordering Information

Part Number	Description
• NW700LQ	DV Decoder
• Daytona Beach	Manufacturing Kit



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