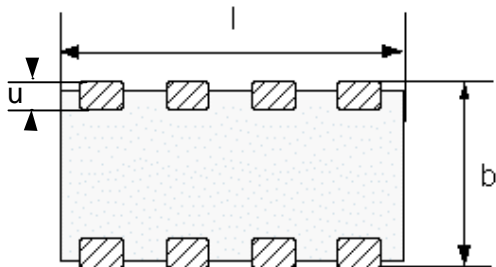


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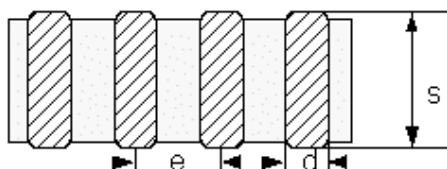
(parameters may be changed if necessary)

Designation System:

- CA = **C**hip **A**rray
 06 = Dimensions of the device **06**x**12** (Length x width in 1/100 inch)
 P = Design (**P**arallel internal structure)
 4 = Number of elements
 S = **S**pecial tolerance of the varistor voltage
 17 = Max. operating voltage
 T = **T**hree-layer-termination
 LC = **L**ow **C**apacitance
 G = Taped version (blister tape, 7" reel, 3000 pieces/reel)

Figure:


$$\begin{aligned}
 l &= 3,2 \pm 0,2 \\
 b &= 1,6 \pm 0,15 \\
 s &= 0,9 \text{ max.} \\
 d &= 0,4 \pm 0,15 \\
 e &= 0,8 \pm 0,15 \\
 u &= 0,2 \pm 0,1
 \end{aligned}$$



(All dimensions in mm)

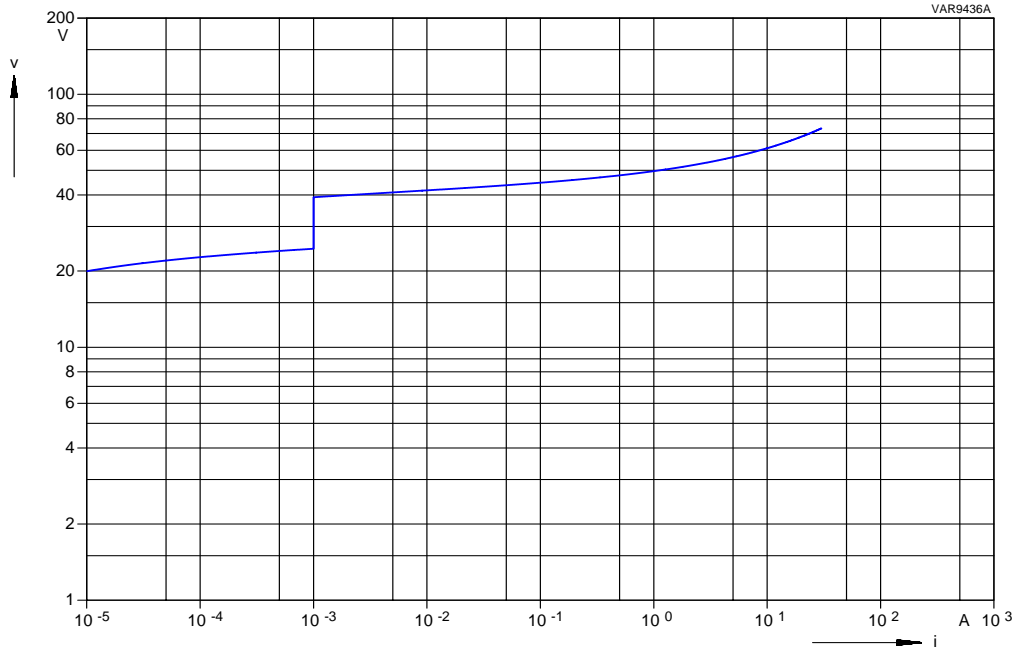
As far as patents or other rights of third parties are concerned, liability is only assumed for components per se, not for applications, processes and circuits implemented within components or assemblies. The information describes the type of component and shall not be considered as assured characteristics. Terms of delivery and rights to change design reserved.

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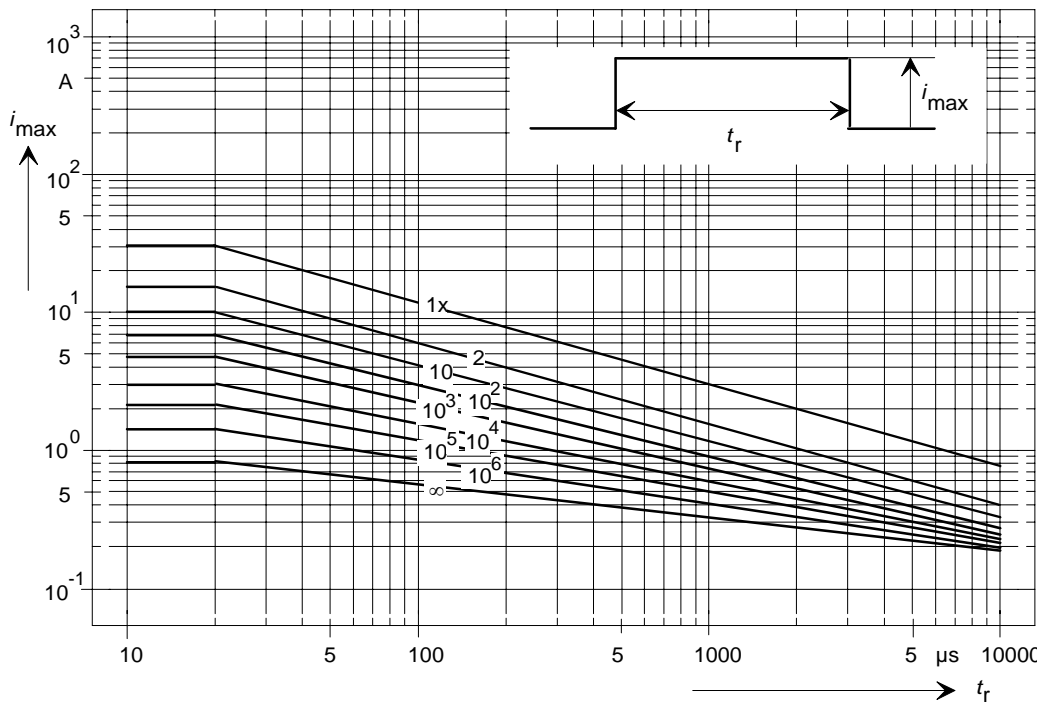
Preliminary data sheet

(parameters may be changed if necessary)

V-I-Characteristic:



Derating field:





Preliminary data sheet

(parameters may be changed if necessary)

Electrical Data

Max. operating voltage

RMS voltage

$V_{eff} = 17\text{ V}$

DC voltage

$V_{DC} = 22\text{ V}$

Varistor voltage (@ 1 mA)

$V_V = 25 - 40\text{ V}$

Max. clamping voltage (@ 1 A)

$V_C = 50\text{ V}$

Max. average power dissipation

$P_{max} = 3\text{ mW}$

Max. surge current (8/20 μs)

$\hat{I}_{max} = 1 \times 30\text{ A}$

Max. energy absorption (2 ms)

$E_{max} = 1 \times 0.075\text{ J}$

Capacitance (@ 1MHz, 0.5 V)

$< 75\text{ pF}$

Response time

$< 0.5\text{ ns}$

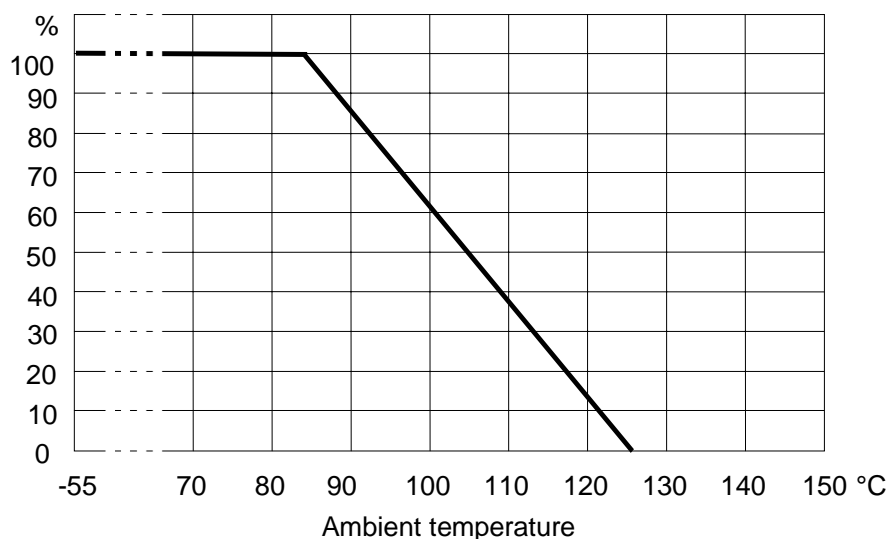
Operating temperature

$-40 \dots +85\text{ }^\circ\text{C}$

Storage temperature (mounted parts)

$-40 \dots +125\text{ }^\circ\text{C}$

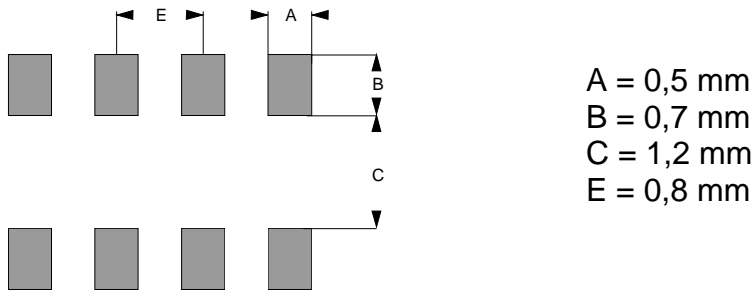
Max. current, energy, operating voltage and average power dissipation depending on ambient temperature



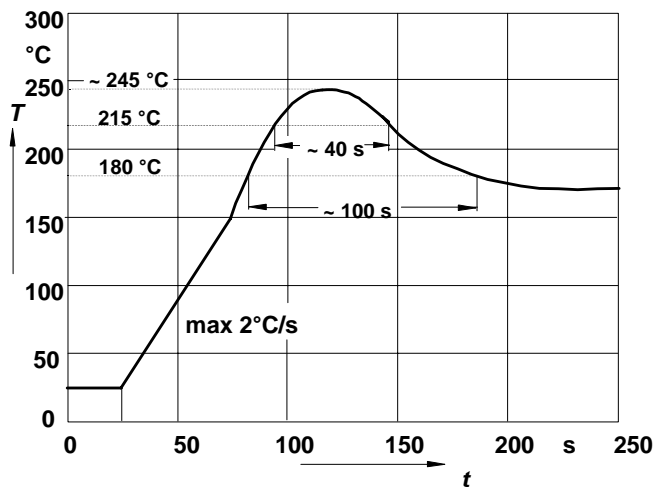
Preliminary data sheet

(parameters may be changed if necessary)

Recommended Geometry of Solder Pads



Recommended Soldering Temperature Profiles



The components should be soldered within 12 months after delivery from EPCOS. The parts are to be left in the original packing in order to avoid any soldering problems caused by oxidized terminals.

Storage temperature: -25 to 45°C.

Relative humidity: <75% annual average, <95% on max. 30 days in a year.

The usage of mild, non activated fluxes for soldering is recommended, as well as proper cleaning of the PCB.

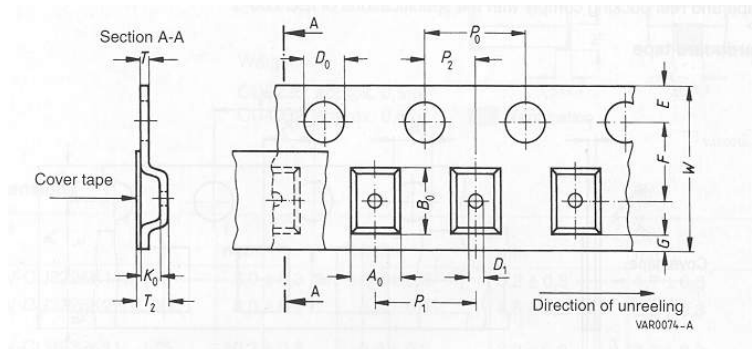
Preliminary data sheet

(parameters may be changed if necessary)

Taping and Packaging:

Taping: Tape and reel packing according to IEC 60286-3

Tape material: Blister

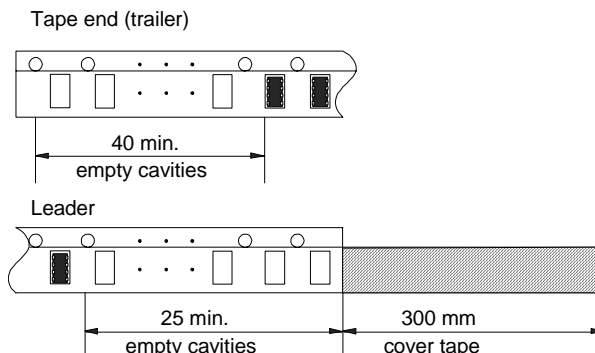


Dimensions and tolerances:

Definition	Symbol	Dimension [mm]	Tolerance [mm]
Compartment width	A_0	1.9	± 0.2
Compartment length	B_0	3.5	± 0.2
Compartment height	K_0	1.3	max.
Sprocket hole diameter	D_0	1.5	$+0.1 / -0$
Compartment hole diameter	D_1	1.0	min.
Sprocket hole pitch	P_0	4.0	± 0.1 ¹⁾
Distance center hole to center compartment	P_2	2.0	± 0.05
Pitch of the component compartments	P_1	4.0	± 0.1
Tape width	W	8.0	± 0.3
Distance edge to center of hole	E	1.75	± 0.1
Distance center hole to center compartment	F	3.5	± 0.05
Distance compartment to edge	G	0.75	min.
Overall thickness	T_2	2.5	max.
Thickness tape	T	0.3	max.

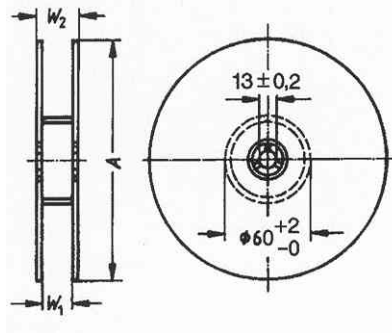
¹⁾ $\leq \pm 0.2$ mm over any 10 pitches

Package: 8 mm tape:



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(parameters may be changed if necessary)

Packing material: Plastic

Reel Dimensions:

Definition	Symbol	Dimension [mm]	Tolerance [mm]
Reel diameter	A	180	-3
Reel width (inside)	W_1	8.4	+1.5 /-0
Reel width (outside)	W_2	14.4	max.

Packing unit: 3000 pcs / reel

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