

256 Megabit Synchronous DRAM

DPSD8MX32RY5

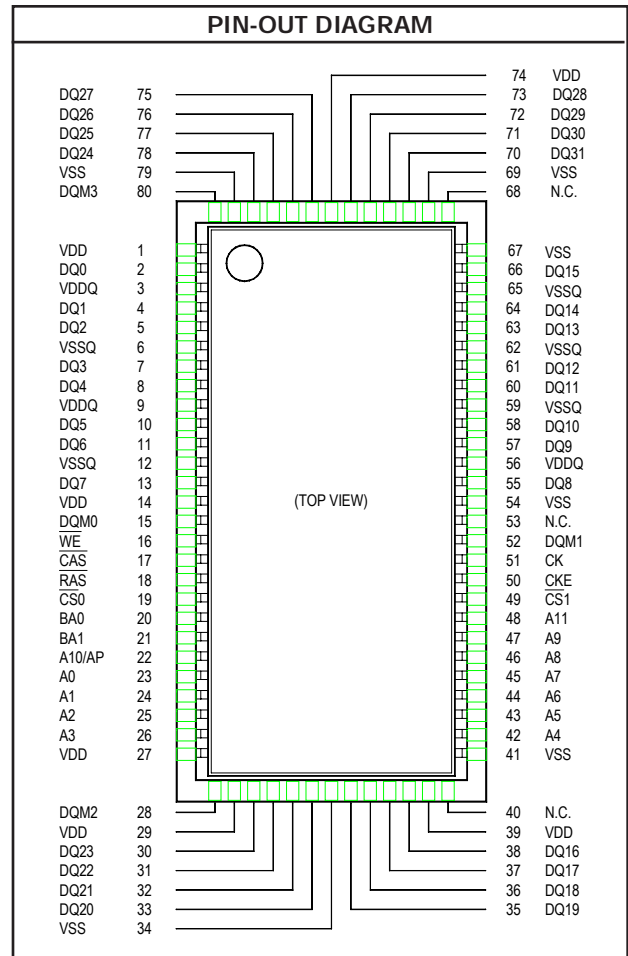
DESCRIPTION:

The LP-Stack™ series is a family of interchangeable memory modules. The 256 Megabit SDRAM is a member of this family which utilizes the new and innovative space saving TSOP stacking technology. The modules are constructed with 4 Meg x 16 SDRAMs.

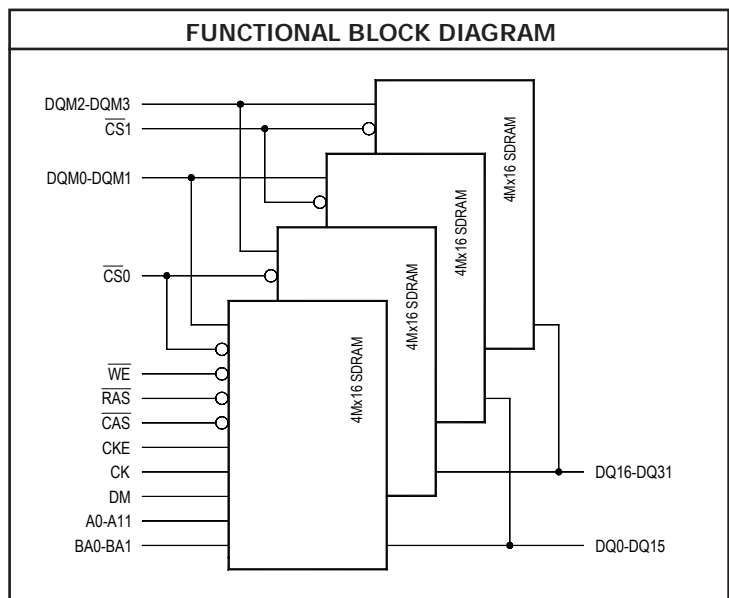
This 128 Megabit LP-Stack™ module, DPSD8MX32RY5, has been designed to allow 32 Output Data lines utilizing two 4 Meg x16 SDRAM TSOP monolithics. Utilizing this LP-Stack™ family allows the memory board designer to upgrade the memory in their products without redesigning the memory board, thus saving time and money.

FEATURES:

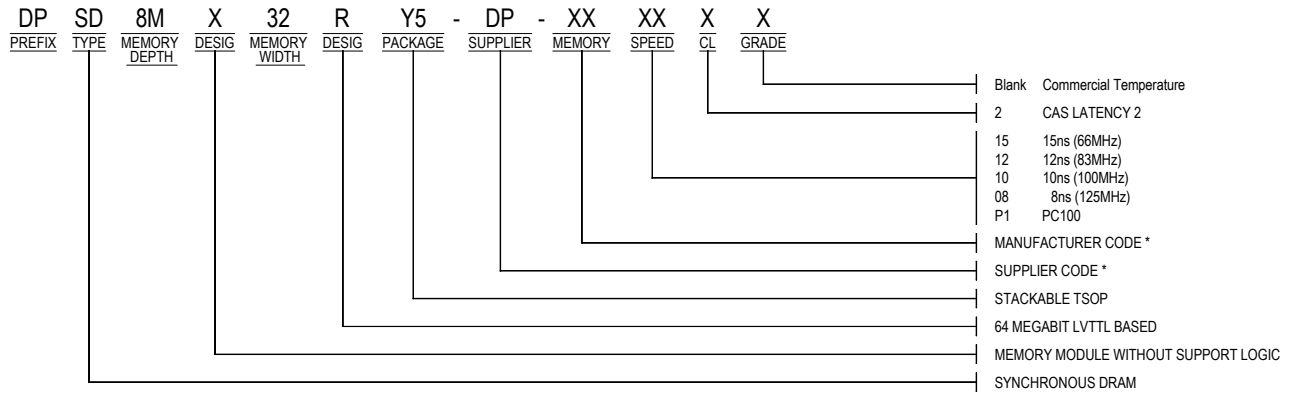
- Configuration Available:
8 Meg x 32 bit
- Clock Frequency:
66, 83, 100, 125 MHz (max.)
- PC100 and PC133 Compatible
- 3.3V Supply
- LVTTTL Compatible I/O
- Four Bank Operation
- Programmable Burst Type, Burst Length, and CAS Latency
- 4096 Cycles / 64 ms
- Auto and Self Refresh
- Package: TSOP Leadless Stack



PIN NAMES	
A0-A11	Row Address: A0-A11 Column Address: A0-A7
BA0,BA1	Bank Select Address
DQ0 - DQ1	Data In/Data Out
CS	Chip Select
CAS	Column Address Strobe
RAS	Row Address Enable
WE	Data Write Enable
DQM0 - DQM3	Upper Data Input/Output Masks
CKE	Clock Enable
CK	System Clock
VDD/VSS	Power Supply/Ground
VDDQ/VSSQ	Data Output Power/Ground

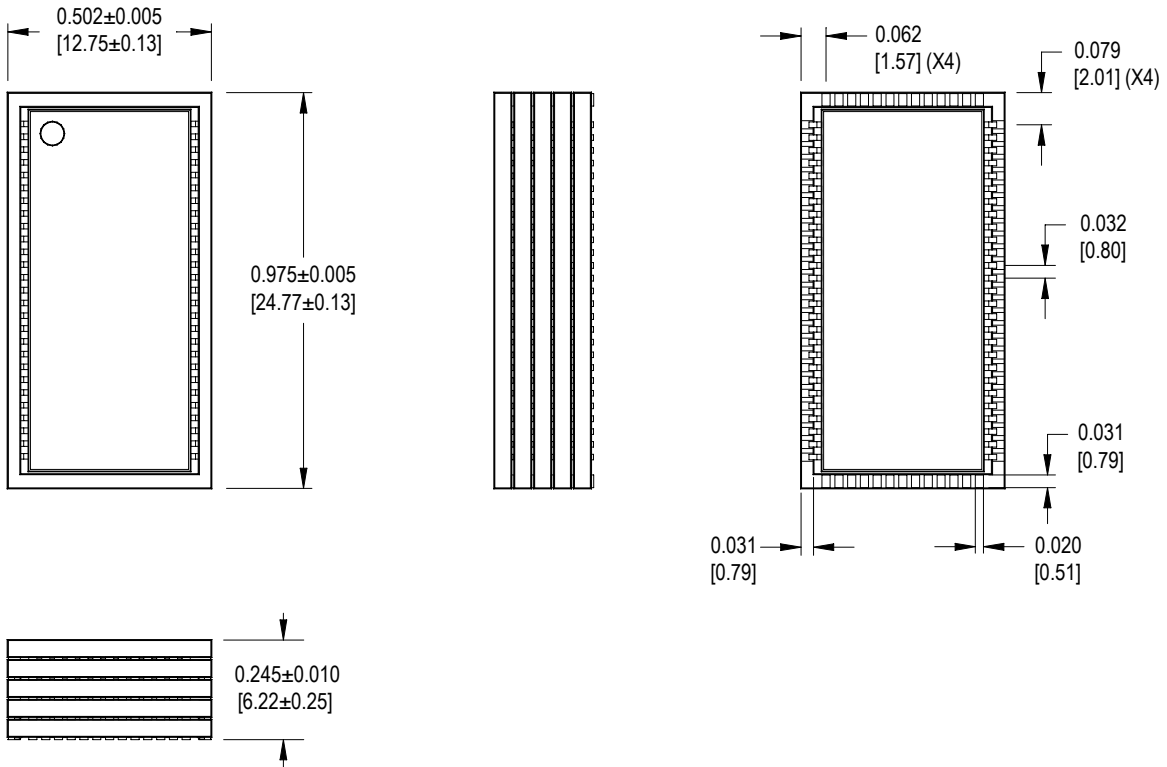


ORDERING INFORMATION



* Contact your sales representative for supplier and manufacturer codes.

MECHANICAL DRAWING



Dimensions - Inches [mm]

P R E L I M I N A R Y