

256 Megabit CMOS DDR SDRAM

DPDD32MX8TSBY5

DESCRIPTION:

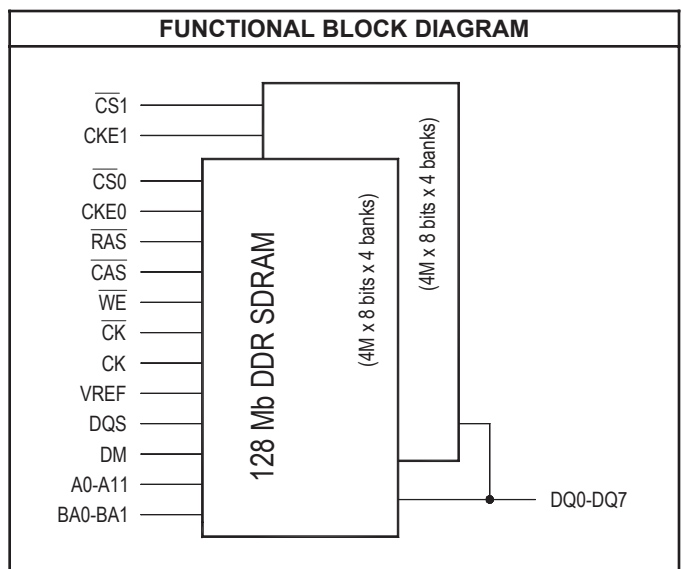
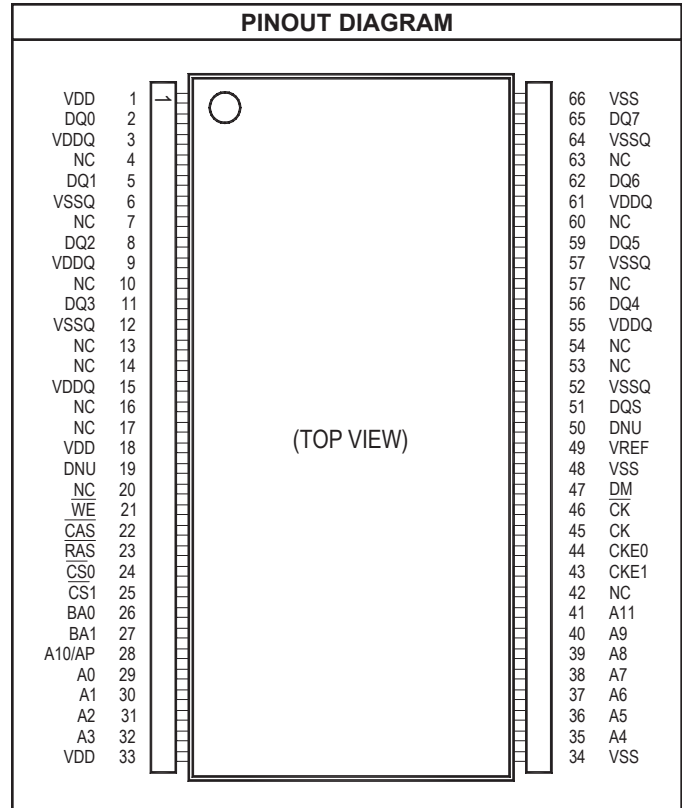
The Memory Stack™ series is a family of interchangeable memory devices. The 256 Mb, CMOS DDR Synchronous DRAM assembly utilizes the space saving LP-Stack™ technology to increase memory density. This stack is constructed with two 128Mb (16M x 8) DDR SDRAMs.

This 256 Mb LP-Stack™, has been designed to fit in the same footprint as the 128Mb (16M x 8) DDR SDRAM TSOPII monolithic. This allows system upgrade without electrical or mechanical redesign, providing an alternative low cost memory solution.

FEATURES:

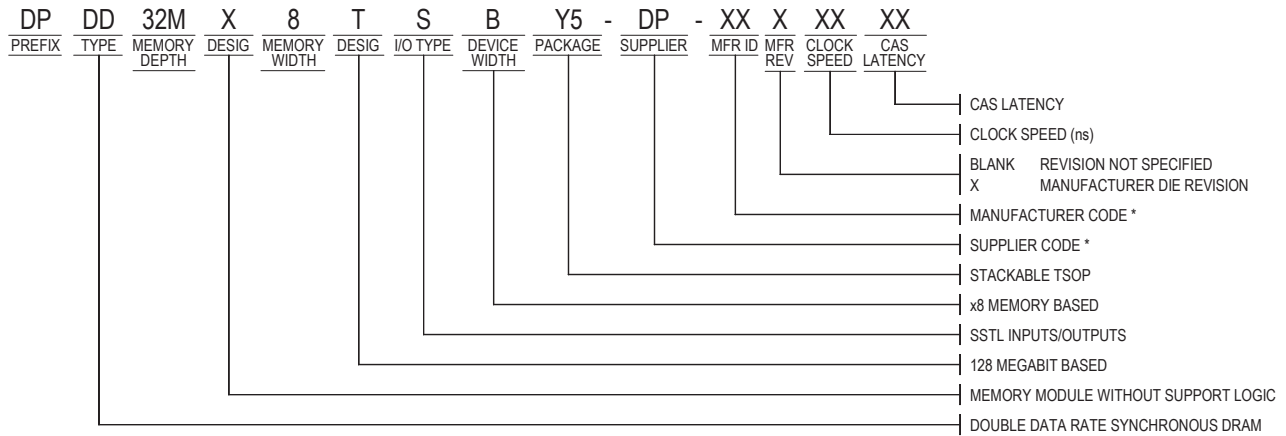
- Electrical characteristics meet semiconductor manufacturers' datasheet
- Memory organization:
 - (2) 128Mb memory devices. Each device arranged as 16M x 8 bits (4M x 8 bits x 4 banks)
- Memory stack organization:
 - 32M x 8 bits (8M x 8 bits x 4 banks)
- JEDEC approved, 2 Rank stack pinout and footprint (with 2 CSs and 2 CKEs)
- Optimized for RDIMMs
- IPC-A-610, class 2, manufacturing standards
- Lead free manufacturing process
- Package: 66-Pin TSOPII stack

PIN NAMES	
A0-A11	Row Address: A0-A11 Column Address: A0-A9, A11
BA0,BA1	Bank Select Address
A10/AP	Auto Precharge
DQ0-DQ7	Data In/Data Out
CAS	Column Address Strobe
CS0, CS1	Chip Select
RAS	Row Address Strobe
WE	Data Write Enable
CK, CK	Differential Clock Inputs
CKE0, CKE1	Clock Enable
DQS	Data Strobe
DM	Data Masks
VDD	Power Supply
VSS	Ground
VDDQ	DQ Power Supply
VSSQ	DQ Ground
VREF	SSTL_2 Reference Voltage
DNU	Do Not Use
NC	No Connect



A D V A N C E D I N F O R M A T I O N

ORDERING INFORMATION



* Contact your sales representative for supplier and manufacturer codes.

NOTES:

1. AC Parameters of base memory are unchanged from device manufacturers' specifications.
2. DC Parameters may be affected by stacking. Please refer to application note 53A004-00 for further information.
3. For assembly and inspection procedures, refer to application note 53A001-00.
4. Maximum reflow temperature recommendation is 215°C.

MECHANICAL DIAGRAM

