Schottky barrier diode RB721Q-40

Applications

High speed switching

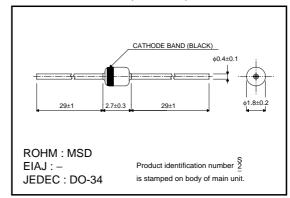
Features

- 1) Glass-sealed envelope for high reliability. (MSD)
- 2) Small pitch enables insertion on PCBs.
- 3) Low VF and low IR.

Construction

Silicon epitaxial planar

●External dimensions (Units : mm)



● **Absolute maximum ratings** (Ta=25°C)

| Parameter | Symbol | Limits | Unit |
|-----------------------------|-----------------|-------------------|------|
| Peak reverse voltage | V _{RM} | 40 | V |
| DC reverse voltage | Vr | 40 | V |
| Mean rectifying current | lo | 30 | mA |
| Peak forward surge current* | IFSM | 200 | mA |
| Junction temperature | Tj | 125 | °C |
| Storage temperature | Tstg | -40 ~ +125 | °C |

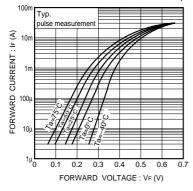
 $[\]ast$ 60 Hz for 1 \bigcirc

● Electrical characteristics (Ta=25°C)

| Parameter | Symbol | Min. | Тур. | Max. | Unit | Conditions |
|-------------------------------|--------|------|------|------|------|----------------------------|
| Forward voltage | VF | - | _ | 0.37 | V | I _F =1mA |
| Reverse current | lR | _ | _ | 0.5 | μΑ | V _R =25V |
| Capacitance between terminals | Ст | _ | 2.0 | _ | pF | V _R =1V, f=1MHz |

Note) ESD sensitive product handling required.

●Electrical characteristic curves (Ta=25°C)



(2) 10µ Ta=100°C

Ta=15°C

Ta=25°C

Ta=25°C

Ta=25°C

REVERSE VOLTAGE: VR (V)

Typ. pulse

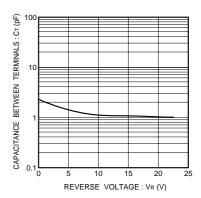


Fig.1 Forward characteristics

Fig.2 Reverse characteristics

Fig.3 Capacitance between terminals characteristics

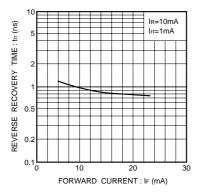


Fig.4 Reverse recovery time characteristics

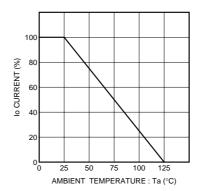


Fig.5 Derating curve (mounting on glass epoxy PCBs)

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