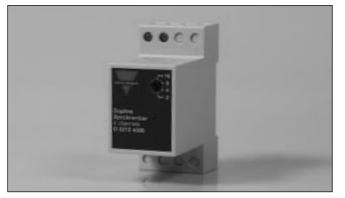
Synchronizer Module Type D 3212 4000



D 3212 4000



Product Description

Dupline[®] transmitter with multiplexing function for multiplexed transmission of analog signals. Applicable for transmitters and display with analog inputs and built-in demultiplexer (type D 6369 6475 or D 3429 61..). Increases the transmission capacity of a Dupline[®] system to max. 112 analog signals with 12 bit resolution.

Type Selection

Supply	Ordering no. 4 channels Synchronizer
By Dupline®	D 3212 4000

Code module*

* No code module required, as the synchronizer always transmits on channels A 1-4.

Product Specifications

Dupline connections Signal Common	Terminal 1 Terminal 2
Selector setting	Front switch
2 addresses	2
4 addresses	4
8 addresses	8
16 addresses	16
Multiplex frequency	1 pulse train

Supply Specifications

Power supply

Reverse polarity protection Rated operational current Power dissipation

	Supplied by Dupline [®]
	≤ 300 μA
<	≦10 mW

- Multiplexer for analog modules
- 2, 4, 8 or 16 multiplex addresses
- Multiplex addresses on channels A1 to A4
- H2-housing
- For mounting on DIN-rail (EN 50022)
- Supplied by Dupline[®]

Ordering Key

Type: Dupline[®] —

H2-housing

Type no. —

No. of channels

General Specifications

Environment Degree of protection Pollution degree Operating temperature Storage temperature	IP 40 3 (IEC 60664) -20° to +50°C (-4° to +122°F) -50° to +85°C (-58° to +185°F)
Humidity (non-condensing)	20 to 80%
Mechanical resistance Shock Vibration	15 G (11 ms) 2 G (6 to 55 Hz)
Dimensions Material (see "Technical Information")	H2-housing
Weight	75 g

Mode of Operation

Synchronizer for control of multiplexed analog modules in H4-housing.

D 3212 continuously transmits binary values (addresses) on channels A1 to A4 as shown in the chart below. Every binary value enables the respective transmitters, receivers and displays in a system that uses multiplexed modules.

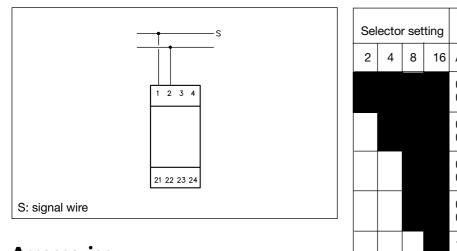
First, the transmitters/receivers on address 0 exchange data. In the following pulse train the transmitters/receivers on address 1 exchange data etc. With this all modules set up to the same multiplex address exchange data for one pulse train on the channel groups they are coded for. The time until the same modules exchange data again depends on the setting of the selector switch at the front of the D 3212.The selector switch defines the number of multiplex addresses that are generated on channels A1 to A4.

Note: Multiplexed modules must not be used in systems where channel generators with 2 or 3 sequences are installed.



Wiring Diagram

Selector Setting



FMD 411

Accessories

DIN-rail

For further information refer to "Accessories".

Sel	ecto	r set	ting	Binary value			Multiplex address	
2	4	8	16	A1	A2	A3	A4	
				0 0	0 0	0 0	0 1	0 1
				0 0	0 0	1 1	0 1	2 3
				0 0	1 1	0 0	0 1	4 5
				0 0	1 1	1 1	0 1	6 7
				1 1	0 0	0 0	0 1	8 9
				1 1	0 0	1 1	0 1	a b
				1 1	1 1	0 0	0 1	c d
				1 1	1 1	1 1	0 1	e f

Operation Diagram

Dupline [®] sequences	2 3	16	18 L
Transmitters/receivers adr. 0 _H			
Transmitters/receivers adr. 1 _H	r	ſ	 -
Transmitters/receivers adr. 2 _H			
Transmitters/receivers adr. f _H			