# 350MHz High Speed Buffer

## CLM4125

### **FEATURES**

CORPORATION

- High Slew Rate...... 3500V/μs
- Wide Bandwidth..... 350MHz
- Peak Output Current ..... 100mA • No Oscillations with Capacitive Loads

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- Fully Specified to Drive 50Ω Lines

### APPLICATIONS

- Pulse and Video Amplifier
- Coaxial Cable Driver
- Video Switching and Routing

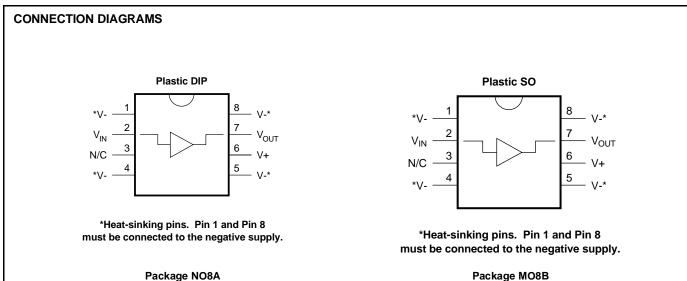
#### **GENERAL DESCRIPTION**

The CLM4125 is a high speed unity gain buffers that slew at 3500V/µs, and a small signal bandwidth of 350MHz.

This device is ideal to drive a active load CRT amplifier.

### **ORDERING INFORMATION**

Part	Package 1	Temperature Range
CLM4125 N	NO8A (Plastic P Dip 8 Lea	d) $-40^{\circ}$ C to $85^{\circ}$ C
CLM4125 M	MO8B (SOIC 8 Lead)	-40°C to 85°C



#### Package MO8B

## CLM4125



#### ABSOLUTE MAXIMUM RATINGS (Note 1)

Supply Voltage ±20
Input Voltage ±Vsupply
Storage Temperature Range
Lead Temperature
(Soldering 10 seconds) 260°C
Power Dissipation (Note 4)

ESD Tolerance (Note 3)	±2000V
Thermal Resistance ( $\theta_{JA}$ ) (Note 6)	
N Package	50°C/W
M Package	60°C/W
Maximum Junction Temperature	. 150°C

**DC ELECTRICAL CHARACTERISTICS** The following specifications apply for Supply Voltage = +12, -10V,  $V_{CM} = 0$ ,  $R_L \ge 100 K\Omega$  and  $R_S = 50\Omega$  unless otherwise noted. **Boldface** limits apply for  $T_A = T_J = T_{MIN}$  to  $T_{MAX}$ ; all other limits  $T_A = T_J = 25^{\circ}C$ .

SYMBOL	CHARACTERISTICS	ТҮР	Limit (Note 5)	UNITS	CONDITIONS
A <sub>V1</sub>	Voltage Gain 1	0.99	0.96		$R_L = 1K$ , $V_{IN} = \pm 10V$
A <sub>V2</sub>	Voltage Gain 2	0.92	0.89	V/V Min	$R_L = 100\Omega, \ V_{IN} = \pm 10V$
A <sub>V3</sub>	Voltage Gain 3	0.92	0.89		$R_L = 50\Omega, \ V_{IN} = \pm 5V$
Vos	Offset Voltage	10	15 <b>25</b>	mV Max	R <sub>L</sub> = 1K
IB	Input Bias Current	1	5 7	μA Max	$R_L = 1K\Omega, R_S = 10k\Omega,$
R <sub>IN</sub>	Input Resistance	0.3		MΩ	R <sub>L</sub> = 50Ω
CIN	Input Capacitance	3.5		pF	
Ro	Output Resistance	3	5 10	$\Omega$ Max	I <sub>OUT</sub> = ±10mA
I <sub>S1</sub>	Supply Current 1	20	30 <b>30</b>	mA Max	R <sub>L</sub> = ∞
V <sub>O1</sub>	Output Swing 1	11	9	±V Min	R <sub>L</sub> = 1K
V <sub>O2</sub>	Output Swing 2	9	8	<u> </u>	R <sub>L</sub> = 100Ω
V <sub>O3</sub>	Output Swing 3	9	8	V <sub>PP</sub> Min	$R_L = 50\Omega$

#### AC ELECTRICAL CHARACTERISTICS

The following specifications apply for Supply Voltage = +12, -10V,  $V_{CM} = 0$ ,  $R_L \ge 100 K\Omega$  and  $R_S = 50\Omega$  unless otherwise noted. **Boldface** limits apply for  $T_A = T_J = T_{MIN}$  to  $T_{MAX}$ ; all other limits  $T_A = T_J = 25^{\circ}C$ .

SYMBOL	CHARACTERISTICS	ТҮР	Limit (Note 5)	UNITS	CONDITIONS
SR1	Slew Rate 1	3500	3000	V/µs	$V_{IN} = \pm 4.5 V$ , $R_L = 100 \Omega$ (Note 2)
SR <sub>2</sub>	Slew Rate 2	2250	2000	V/μO	$V_{IN} = \pm 4V, R_L = 50\Omega$ (Note 2)
SS <sub>BW</sub>	Small Signal Bandwidth	350	300		$\label{eq:VIN} \begin{array}{l} V_{\text{IN}} = \pm 100 m V_{\text{PP}}, \ R_{L} = 50 \Omega \\ C_{L} \leq 10 p F \end{array}$
LS <sub>BW</sub>	Large Signal Bandwidth	140	120	MHz	$V_{OUT} = \pm 4.5V, R_L = 100\Omega$ $C_L \le 10pF$
P <sub>BW</sub>	Power Bandwidth	130	110		$V_{IN} = \pm 4V, C_L \le 100 pF$
t <sub>r</sub> , t <sub>f</sub>	Rise Time Fall Time	2.5	3.5	ns	$C_L \le 100 pF$ $V_O = 4V_{PP}$
t <sub>pd</sub>	Propagation Delay Time	2.0		ns	$ \begin{array}{l} R_{L} = 50\Omega, \ C_{L} \leq 10 pF \\ V_{O} = 4 V_{PP} \end{array} $
Os	Overshoot	3		%	$ \begin{array}{l} R_{L} = 50\Omega, \ C_{L} \leq 10 pF \\ V_{O} = 100 mV_{PP} \end{array} $

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. DC and AC electrical specifications do not apply when operating the device beyond its rated operating conditions.

**Note 2:** Slew rate is measured with  $50\Omega$  source impedance at  $25^{\circ}$ C. For accurate measurements, the input slew rate should be at least  $5000V/\mu$ s.

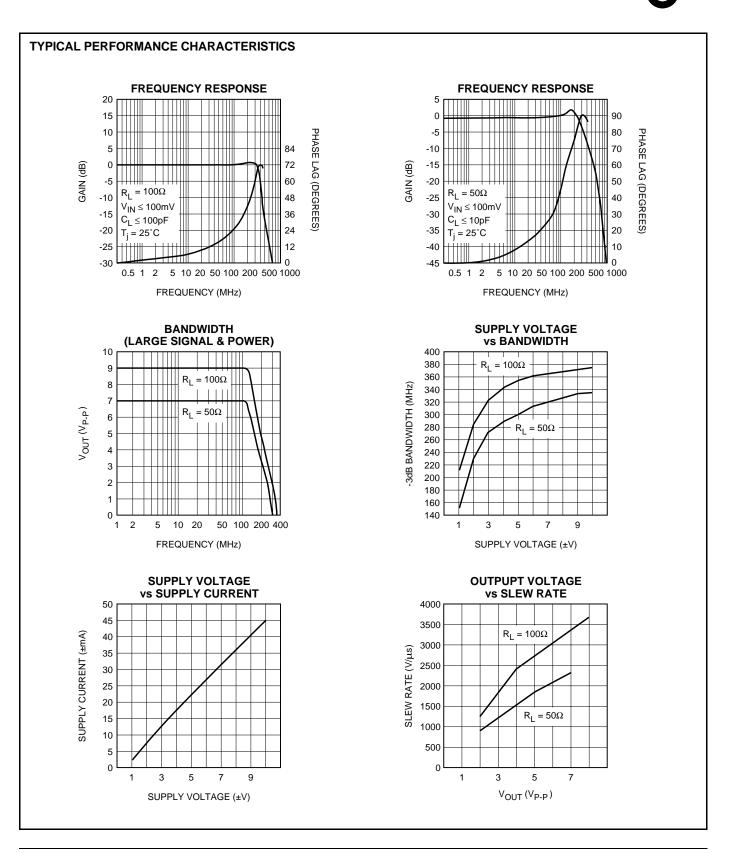
Note 3: The test circuit consists of the human body model of 120 pF in series with  $1500 \Omega$ .

**Note 4:** The maximum power dissipation is a function of  $T_{J(max)}$ ,  $\theta_{JA}$  and  $T_A$ . The maximum allowable power dissipation at any ambient temperature is  $P_D = (T_{J(max)} - T_A)/\theta_{JA}$ .

Note 5: Limits are guaranteed by testing, correlation or periodic characterization.

**Note 6:** For M & N package,  $\theta_{JA}$  is measured by soldering the unit directly on a printed circuit board and V<sup>-</sup> pins are connected to 2 square inches of 2 oz copper.

CLM4125



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