



## L7590C Telephone Ringing Driver

### Features

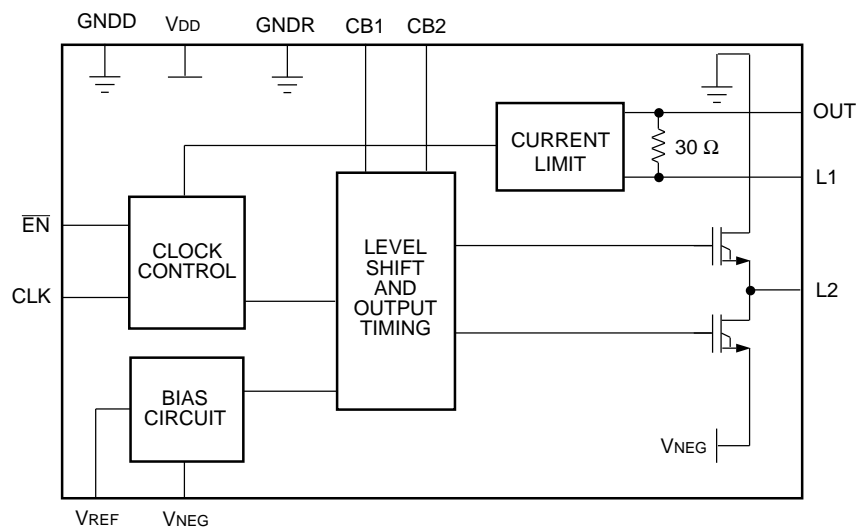
- Applies a battery-backed, single-ended, sinusoidal ringing signal to the telephone loop
- Ringing capability:
  - 40 Vrms into 5 North American REN
  - 30 mA<sub>peak</sub> into 4 German REN
- Built-in current limiting
- Low power dissipation in idle and ringing modes
- Requires 5 V, a high-value negative dc supply, and a digital input sequence

### Description

The L7590C Telephone Ringing Driver produces a low-frequency sinusoidal ringing signal for analog telephone loops. This is accomplished by producing an output pulse stream that is an amplified replica of the digital input. The output is then filtered to produce a sine wave. The peak-to-peak output level is determined by the magnitude of the negative high-voltage dc applied to the device (up to  $-170$  V).

The required input is a 100 kHz CMOS-compatible pulse stream, pulse-width modulated (PWM) at the desired ringing frequency. The output stage consists of one upper and one lower drive transistor with an internal control circuit.

The L7590C IC is optimized for low power dissipation and is available in a 16-pin, surface-mount package.



12-3057a (f)

Figure 1. Block Diagram

## Pin Information

Pin	Symbol	Type	Name/Function
14	V <sub>DD</sub>	—	<b>+5 V Power Supply.</b>
3	GNDD	—	<b>Digital Ground.</b>
12	GNDR	—	<b>Ringing Ground.</b>
8	V <sub>NEG</sub>	—	<b>Negative Power Supply.</b> Determines the swing of the ringing generator. Connect a 1 $\mu$ F capacitor from V <sub>NEG</sub> to V <sub>REF</sub> .
5	V <sub>REF</sub>	—	<b>Internal Reference Voltage.</b> Connect a 1 $\mu$ F capacitor from V <sub>REF</sub> to V <sub>NEG</sub> .
16	CLK	I	<b>Clock.</b> Modulated pulse stream. Low = 0 V. High = V <sub>DD</sub> .
7	OUT	O	<b>Ringing Generator Output.</b> Connect to ringing relay contact through a filter.
1	CB1	—	<b>Bootstrap Capacitor.</b> Connect a 0.1 $\mu$ F capacitor between CB1 and CB2.
2	CB2	—	<b>Bootstrap Capacitor.</b> Connect a 0.1 $\mu$ F capacitor between CB1 and CB2.
9	L1	—	<b>Filter Inductor.</b> Connect an inductor to pin L2.
10	L2	—	<b>Filter Inductor.</b> Connect an inductor to pin L1.
15	EN	—	<b>Not Enable.</b> A logic low activates the internal circuitry. A logic high puts the device in a low-power powerdown mode.

Absolute Maximum Ratings (T<sub>A</sub> = 25 °C)

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. These are absolute stress ratings only. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this data sheet. Exposure to absolute maximum ratings for extended periods may adversely affect device reliability.

Parameter	Min	Typ	Max	Unit
V <sub>DD</sub>	—	7.0	—	V
V <sub>NEG</sub>	—	−200	—	V
Timing Input Voltage	−0.5	—	7.0	V
Storage Temperature Range	−40	—	125	°C
Maximum Junction Temperature	—	150	—	°C
Relative Humidity Range	5	—	95	%

## Recommended Operating Conditions

Parameter	Min	Typ	Max	Unit
Ambient Temperature	−40	—	85	°C
V <sub>DD</sub> Supply Voltage	4.75	5.0	5.25	V
V <sub>NEG</sub> Supply Voltage	−170	—	−130	V

## Electrical Characteristics

Electrical characteristics apply for 25 °C. Unless noted, V<sub>DD</sub> = +5 V, V<sub>NEG</sub> = −170 V,  $\overline{\text{EN}}$  = low, L1 = 20 mH (R<sub>dc</sub> = 20 Ω, C<sub>eff</sub> = 20 pF), CLK = 100 kHz. Positive currents flow into the device. Typical is defined as 25 °C.

Parameter	Min	Typ	Max	Unit
Power Supply—Idle (CLK = H or L, $\overline{\text{EN}}$ = high, dc):				
I <sub>DD</sub>	—	—	1	mA
I <sub>NEG</sub>	—	—	−50	μA
Power Dissipation	—	—	13.5	mW
Power Supply—Active, No Load:				
I <sub>DD</sub>	—	—	3	mA
I <sub>NEG</sub>	—	−3.2	−8	mA
Power Dissipation	—	—	1.38	W
Efficiency = $\{[(V_{\text{OUT}})^2/R_{\text{LOAD}}]/(V_{\text{NEG}} \bullet I_{\text{NEG}})\}$ :				
Sinking Current, R <sub>LOAD</sub> = 1400 Ω Load to GND	40	80	—	%
Sourcing Current, R <sub>LOAD</sub> = 1400 Ω Load to V <sub>NEG</sub>	40	80	—	%
ON-resistance:				
CLK = H, I <sub>OUT</sub> = −20 mA	0	10	20	Ω
CLK = L, I <sub>OUT</sub> = +20 mA	0	10	20	Ω
Current Limit	25	—	40	mA <sub>peak</sub>

## Functional Description

### Ringer Requirements

This discussion highlights some of the requirements in the United States and Germany. Beginning with the telephone set ringer to be powered, the United States requires 40 V<sub>rms</sub> to be delivered to the equivalent of five parallel telephone sets, or 5 REN (Ringer Equivalence Number), that translates to  $1386\ \Omega + 40\ \mu\text{F}$ . In Germany, 30 mA<sub>peak</sub> must be delivered to four parallel telephone sets. In Germany, 4 REN have an impedance of  $450\ \Omega + 3.4\ \mu\text{F}$ . The difference results from the impedance of the telephone-set ringers of the two countries.

### Ringling Requirements

In Germany, both single-ended ringing and balanced ringing can be used. This is not so in the United States, where some ringers on older equipment have a ground connection at the telephone set, and the signal lead is either the Tip or Ring wire of the twisted pair. This has led to the recommended use of single-ended ringing. Both administrations allow ringing with a negative dc level, known as battery-backed ringing, but in the United States, there is a constraint that this level cannot be more negative than  $-75\ \text{V}$ .

With the exception of the United States, most world-wide administrations require an injection of a pure sine wave. Depending on the country, the distortion requirement is in the range of 3% to 5%. In the United States, the wave does not need to resemble a sine wave, but it cannot be a square wave. The shape requirement of the wave in the United States is expressed as a crest factor (c.f.). This is the ratio of the peak to the rms voltage of the signal. The requirement is  $1.2 < \text{c.f.} < 1.6$ .

## Operation

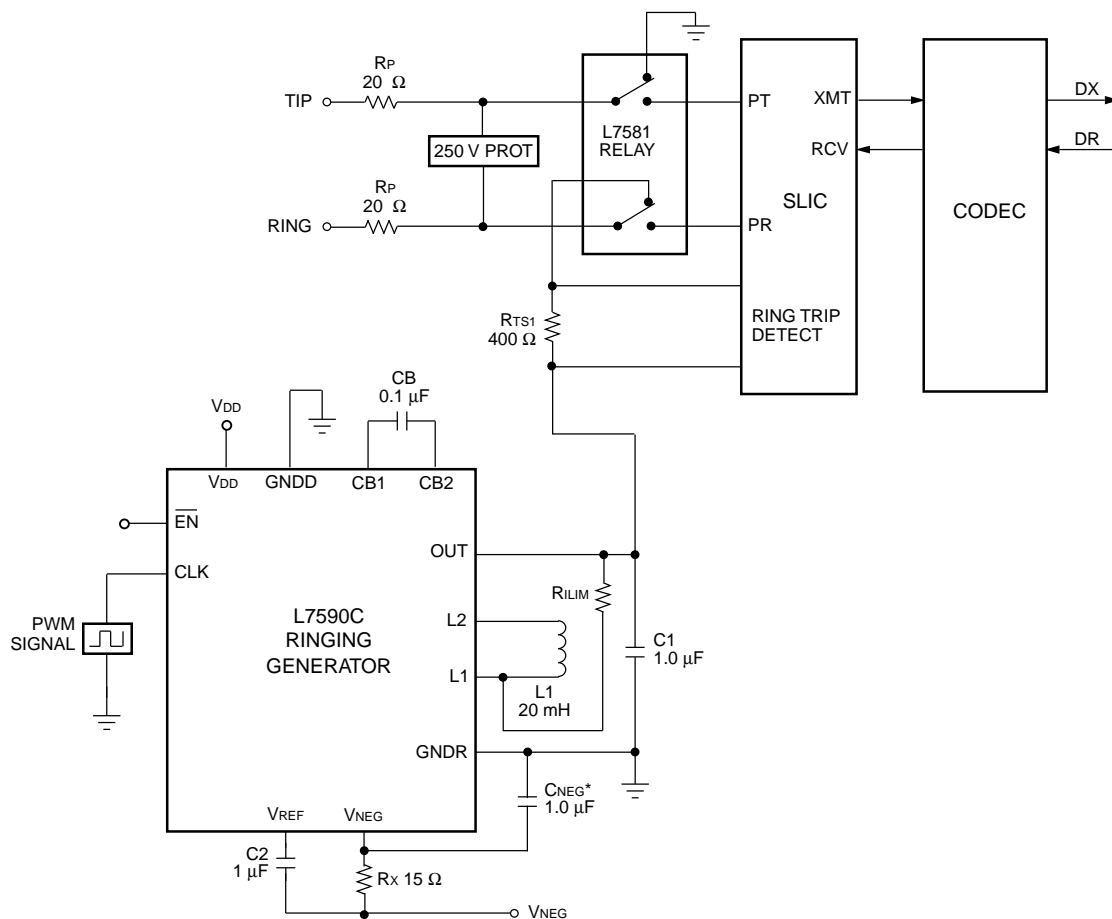
On the L7590C, the prefiltered output is a high-voltage replica of the PWM digital signal that is applied to the CLK input. The signal at OUT is produced by level shifting and control of the high-voltage transistors. The level is determined by the magnitude of the negative dc voltage applied to the output stage transistors. The output, ideally, should be an exact time replica of the CLK input to faithfully reproduce the modulating waveform. This high-voltage replica of the CLK input is filtered to leave only the low-frequency, high-voltage sine wave ringing signal. Figure 3, parts A and B, illustrates the PWM signal with relation to the modulating signal frequency. The PWM signal is at a 100 kHz rate and modulated at the desired ringing signal frequency, typically 20 Hz to 30 Hz. Laboratory equipment generation of the required signal is shown in Figure 5. Propagation delays and switching parameters associated with the control circuitry and output stage transistors can cause skewing which will produce a distorted output of the modulating waveform. Skewing is illustrated in Figure 4, parts A, B, and C. The skewing can be seen as an increase in the duty cycle of the L7590C output compared to the input signal duty cycle.

Using the test arrangement as shown in Figure 5, the harmonic distortion of the filtered output of the L7590C was measured with two different loading circuits. With the North American 5 REN ( $1386\ \Omega + 40\ \mu\text{F}$ ) load, the THD was typically 2.0% to 2.5%. With the Germany 4 REN ( $450\ \Omega + 3.4\ \mu\text{F}$ ) load, the THD was typically 1.5% to 2.0%. The 25 Hz output of the HP\* 3311A generator showed a typical THD of 1.5% when supplying a signal to the HP 8116A.

A  $20\ \Omega$  resistor ( $R_{\text{ILIM}}$ ) must be added between L1 and OUT for 5 REN loads.

\* HP is a registered trademark of Hewlett-Packard Company.

## Application



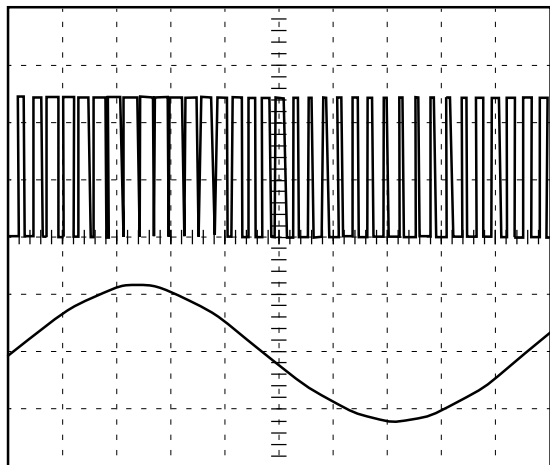
\* CNEG connects to VNEG pin at chip.

12-3058.E (F)

**Figure 2. Basic Battery-Backed Ringing Application**

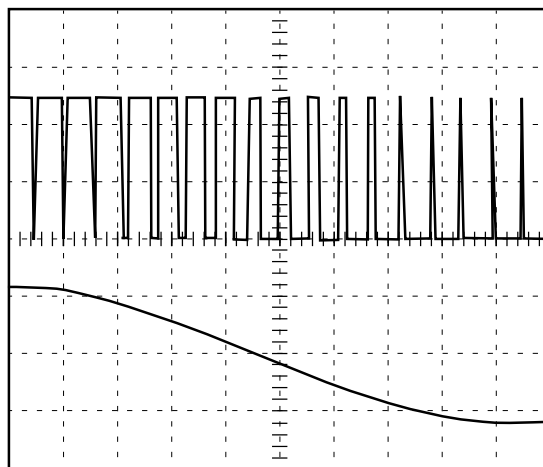
**Table 1. Parts List for Application, Ringing Driver IC**

Name	Value	Function
L1	20 mH	Part of LC output filter
C1	1 μF, 250 V, 20%	Part of LC output filter
C2	1 μF, 20 V, 20%	VREF filter
CB	0.1 μF, 10 V, 20%	Voltage doubling capacitor
Rx	15 Ω, 1 W	Hot insertion limiter
RILIM	20 Ω to 100 Ω	Current-limiter adjustment

**Application** (continued)

12-3381 (F)

**A. Upper = PWM Signal Centered at 100 kHz  
Lower = Modulating Signal**

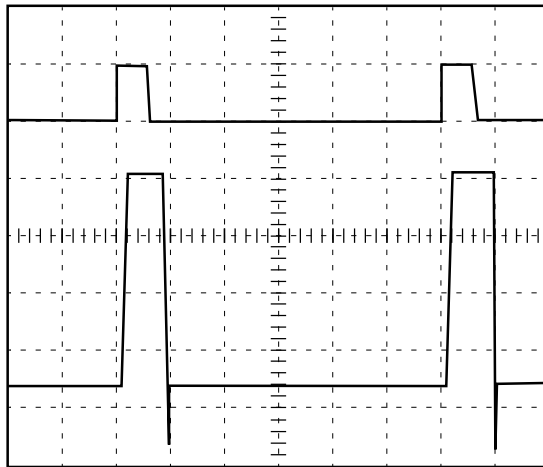


12-3380 (F)

**B. Same as A but Expanded**

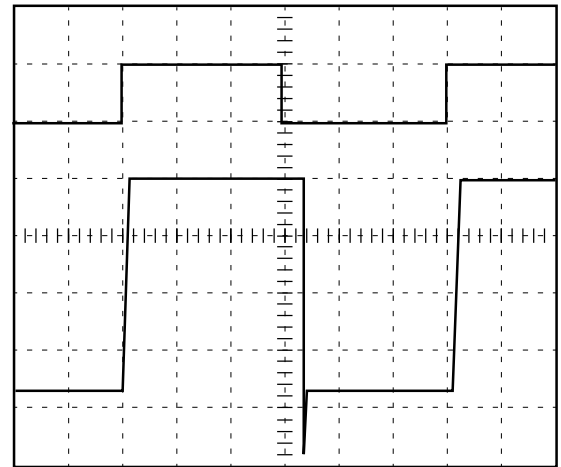
**Figure 3. Modulation Waveforms**

## Application (continued)



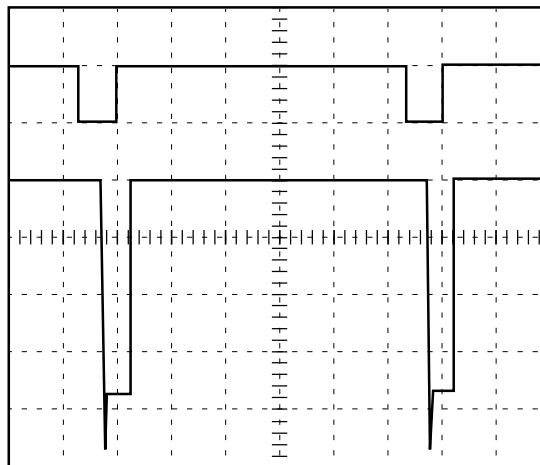
12-3379 (F)

**A. Upper = 10% Duty Cycle—CLK Input  
Lower = L7590C Output**



12-3377 (F)

**B. 50% Duty Cycle**



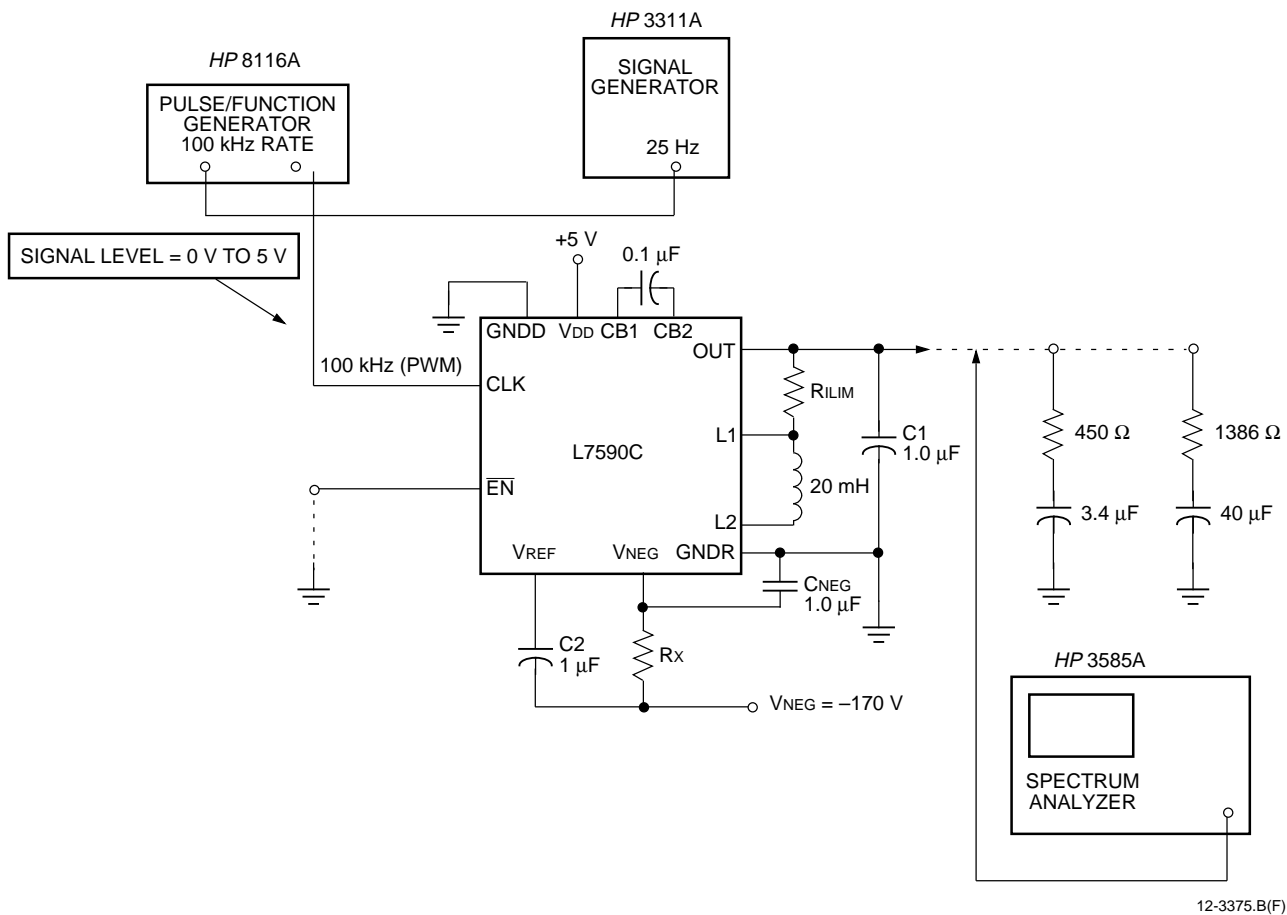
12-3376 (F)

**C. 90% Duty Cycle**

Note: For all three illustrations above, the positive portion of the L7590C output is skewed to be greater than the CLK input.

**Figure 4. Skewing Illustration Waveforms**

Application (continued)



Notes:

$$\text{THD \%} = \frac{\sqrt{E_2^2 + E_3^2 + E_4^2 + \dots}}{\sqrt{E_1^2 + E_2^2 + E_3^2 + E_4^2 + \dots}} \times 100.$$

RX = 15 Ω.

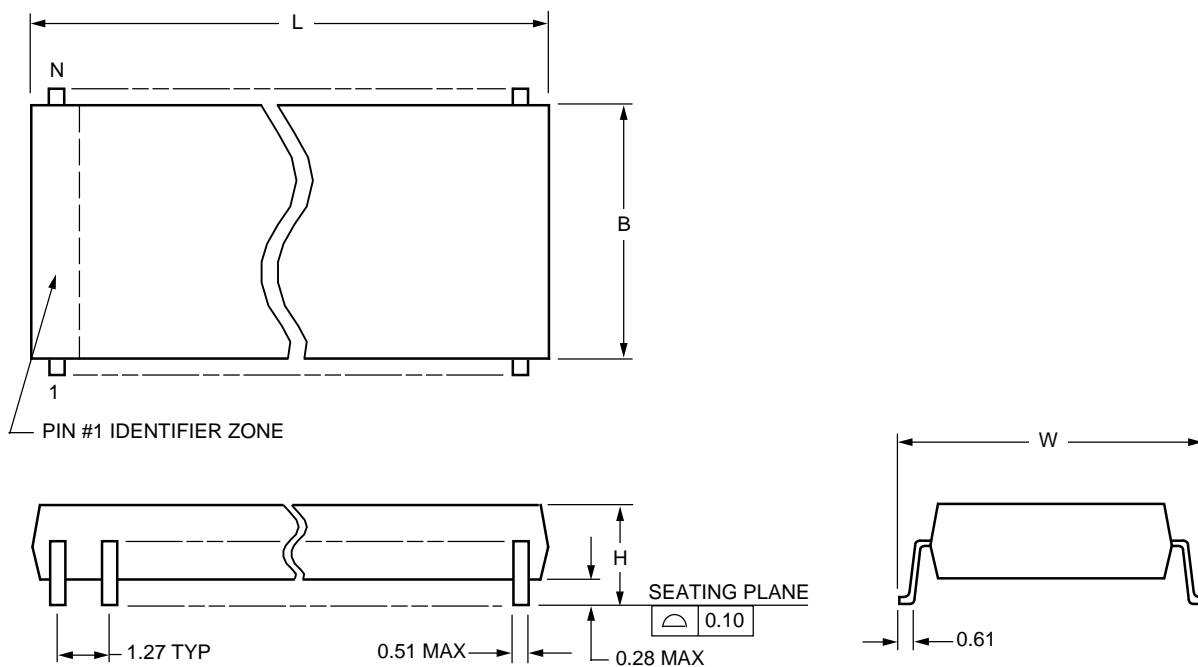
Figure 5. L7590C Test Connections



## Outline Diagram

### 16-Pin, Plastic SOG

Dimensions are in millimeters.



Number of Pins (N)	Maximum Length (L)	Maximum Width Without Leads (B)	Maximum Width Including Leads (W)	Maximum Height Above Board (H)
16	10.49	7.62	10.64	2.67

5-4414r2 (C)

## Ordering Information

Device Part No.	Description	Package	Comcode
LUCL7590CAE-D	Telephone Ringing Driver	16-Pin SOG	108191107
LUCL7590CAE-DT	Telephone Ringing Driver	16-Pin SOG (Tape and Reel)	108191115

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